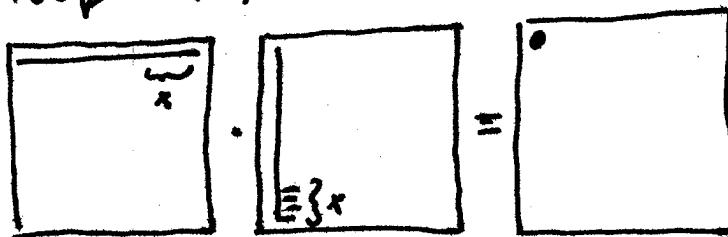


# Why blocking?

assume: cache size  $64\text{u}$   
 cache line = 8 doubles  
 only 1 cache

$\text{C17} = \text{cache miss}$

## 1.) Triple loop $MIM$



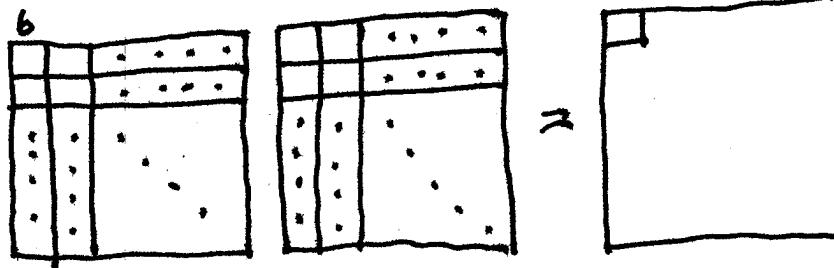
1. entry:  $\frac{n}{8} + n$  C17s (compulsory)

afterwards:  $x$  is in cache

2. entry: no reuse, so again  $\frac{n}{8} + n$  C17s

$$\Rightarrow \text{total} = \left(\frac{n}{8} + n\right)n^2 = \frac{3}{8}n^3 \text{ C17s}$$

## 2.) Blocked $MIM$



choose:  $b \geq 8$   
 (cache line)  
 and  $8 \mid b$   
 and  $3b^2 \leq c$   
 $c = \text{cache size}$

$$1. \text{ block: } \frac{nb}{8} + \frac{4b}{8} = \frac{5b}{4} \text{ C17s}$$

2. block: same

$$\Rightarrow \text{total} = \frac{nb}{4} \cdot \left(\frac{n}{b}\right)^2 = \frac{n^3}{4b}$$

$$\text{choose } b = \sqrt{\frac{c}{3}} \Rightarrow \frac{\sqrt{3}}{4\sqrt{c}} n^3 \text{ C17s}$$

$$\text{gain: } \approx 2.5\sqrt{c}$$

- Explains much of triple loop's poor performance (the other major optimization is unrolling and scalar replacement for better instruction parallelism and register usage)

- Blocking achieves both: better spatial and better temporal locality with respect to the cache