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Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting

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Given the electrical circuit:

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting

Given the electrical circuit:



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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting

Given the electrical circuit:



The circuit contains a constant voltage source, u₀, and a dependent current source, i₄, that depends on the voltage across the capacitor, C, and the resistor, R₃.

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting

Given the electrical circuit:



The circuit contains a constant voltage source, u₀, and a dependent current source, i₄, that depends on the voltage across the capacitor, C, and the resistor, R₃.

Write down the element equations for the seven circuit elements. Since the voltage u₃ is common to two circuit elements, these equations contain 13 rather than 14 unknowns. Add the voltage equations for the three meshes and the current equations for three of the four nodes.

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting II

Draw the structure digraph of the DAE system, and apply the Tarjan algorithm to sort the equations both horizontally and vertically. Write down the causal equations, i.e., the resulting ODE system.

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting II

- Draw the structure digraph of the DAE system, and apply the Tarjan algorithm to sort the equations both horizontally and vertically. Write down the causal equations, i.e., the resulting ODE system.
- Simulate the ODE system across 50 µsec using RKF4/5 with Gustaffsson step-size control and with zero initial conditions on both the capacitor and the inductor.

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting II

- Draw the structure digraph of the DAE system, and apply the Tarjan algorithm to sort the equations both horizontally and vertically. Write down the causal equations, i.e., the resulting ODE system.
- Simulate the ODE system across 50 µsec using RKF4/5 with Gustaffsson step-size control and with zero initial conditions on both the capacitor and the inductor.
- Plot the voltage u_3 and the current i_C , and the step size h on three separate subplots as functions of time.

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Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting III

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Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting III



1:	<u>и</u> 0	-	10
2:	u_1	=	$R_1 \cdot i_1$
3:	u 2	=	$R_2 \cdot i_2$
4:	u ₃	=	$R_3 \cdot i_3$
5:	i _C	=	$C \cdot \frac{du_3}{dt}$
6:	uL	=	$C \cdot \frac{du_3}{dt} \\ L \cdot \frac{dl_L}{dt}$
7:	<i>i</i> 4	=	$4 \cdot u_3$
8.	110	_	111 ± 112
8:	u 0	=	$u_1 + u_3$
9:	u ₀ u _L	=	$u_1 + u_3 \\ u_1 + u_2$
9:	uL	=	$u_1 + u_2$
9: 10:	и _L и2	=	$u_1 + u_2 \\ u_3 + u_4$

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting IV

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1: 2: 3: 4: 5:	u ₀ u ₁ u ₂ u ₃ i _C		10 $R_1 \cdot i_1$ $R_2 \cdot i_2$ $R_3 \cdot i_3$ $C \cdot \frac{du_3}{dt}$ $L \cdot \frac{dl_1}{dt}$
6:	иL	=	$L \cdot \frac{L}{dt}$
7:	i4	=	4 · <i>u</i> ₃
8:	<u>и</u> 0	=	$u_1 + u_3$
9:	uL	=	$u_1 + u_2$
10:	u 2	=	$u_3 + u_4$
11:	<i>i</i> 0	=	$i_1 + i_L$
12:	i_1	=	$i_2 + i_C + i_3$
13:	<i>i</i> 4	=	$i_2 + i_L$

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting IV

1:	и 0	=	10
2:	u 1	=	$R_1 \cdot i_1$
3:	u 2	=	$R_2 \cdot i_2$
4:	u ₃	=	$R_3 \cdot i_3$
5:	i _C	=	$C \cdot \frac{du_3}{dt}$
6:	uL	=	$C \cdot \frac{du_3}{dt} \\ L \cdot \frac{dl_L}{dt}$
7:	<i>i</i> 4	=	$4 \cdot u_3$
8:	и 0	=	$u_1 + u_3$
9:	uL	=	$u_1 + u_2$
10:	u 2	=	$u_3 + u_4$
11:	<i>i</i> 0	=	$i_1 + i_L$
12:	<i>i</i> 1	=	$i_2 + i_C + i_3$
13:	<i>i</i> 4		$i_2 + i_1$



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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting V

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Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting V





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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting VI

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting VI





Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting VII

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting VII





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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting VIII

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Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting VIII





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Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting IX

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting IX





Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting X

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Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting X



1:	и 0	=	10
6:	<i>u</i> 1	=	$R_1 \cdot i_1$
7:	u 2	=	$R_2 \cdot \frac{i_2}{i_2}$
2:	u ₃	=	R₃ · i₃
10:	ic	=	$C \cdot \frac{du_3}{dt} \\ L \cdot \frac{dl_L}{dt}$
11:	u _l	=	$L \cdot \frac{di_L}{di_L}$
3:	i4	=	$4 \cdot \frac{dt}{u_3}$
4:	и 0	=	$u_1 + u_3$
8:	uL	=	$u_1 + u_2$
12:	u 2	=	<u>u</u> ₃ + <u>u</u> ₄
13:	i ₀	=	$i_1 + i_L$
9:	<i>i</i> 1	=	$i_2 + i_C + i_3$
5:	i4	=	$i_2 + i_1$

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting XI

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1: 6: 7: 2: 10: 11: 3:	u ₀ u ₁ u ₂ u ₃ i _C u _L i ₄	= = = = =	$ \begin{array}{c} 10\\ R_1 \cdot i_1\\ R_2 \cdot i_2\\ R_3 \cdot i_3\\ C \cdot \frac{du_3}{dt}\\ L \cdot \frac{di_1}{dt}\\ 4 \cdot u_3 \end{array} $
4:	u ₀	=	$u_1 + u_3$
8:	u _L	=	$u_1 + u_2$
12:	u ₂	=	$u_3 + u_4$
13:	i ₀	=	$i_1 + i_L$
9:	i ₁	=	$i_2 + i_C + i_3$
5:	i ₄	=	$i_2 + i_L$

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting XI

1: 6: 7: 2:	u ₀ u ₁ u ₂	=	$ \begin{array}{c} 10 \\ R_1 \cdot \mathbf{i_1} \\ R_2 \cdot \mathbf{i_2} \\ R_2 \cdot \mathbf{i_2} \end{array} $
10:	и _з i _C	=	$R_3 \cdot i_3$ $C \cdot \frac{du_3}{dt}$ $L \cdot \frac{di_L}{dt}$
11: 3:	u _L i4	=	$L \cdot \frac{dL}{dt}$ $4 \cdot u_3$
4: 8: 12:	u ₀ u _L u ₂	=	$u_1 + u_3$ $u_1 + u_2$ $u_3 + u_4$
13: 9: 5:	i ₀ i ₁ i4	=	$i_1 + i_L$ $i_2 + i_C + i_3$ $i_2 + i_L$

1:	и 0	=	10
2:	i ₃	=	$\frac{1}{R_3} \cdot \boldsymbol{u_3}$
3:	i4	=	4 · <i>u</i> ₃
4:	u_1	=	$u_0 - u_3$
5:	i 2	=	$i_4 - i_L$
6:	<i>i</i> 1	=	$\frac{1}{R_1} \cdot \boldsymbol{u_1}$
7:	u 2	=	$R_2 \cdot i_2$
8:	иL	=	$u_1 + u_2$
9:	i _C	=	$i_1 - i_2 - i_3$
10:	i _C du ₃ dt di _L dt	=	$\frac{\frac{1}{C}}{\frac{1}{L}} \cdot \frac{i_C}{u_L}$
11:	$\frac{di_{l}}{dt}$	=	$\frac{1}{L} \cdot u_L$
12:	u_4	=	$\bar{u}_2 - u_3$
13:	<i>i</i> 0	=	$i_1 + i_L$

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Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting XII

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The model and output equations can be coded as follows:

```
function [xdot] = st_eq2(x, t)
R1 = 100; R2 = 100; R3 = 30;
C = 1e - 6; L = 0.01;
%
u^3 = x(1); \quad iL = x(2);
%
u0 = 10:
i3 = u3/R3;
i4 = 4 * \mu_3
u1 = u0 - u3:
i2 = i4 - iL;
i1 = u1/R1;
u^2 = R^2 * i^2:
uL = u1 + u2;
iC = i1 - i2 - i3:
du3 = iC/C:
diL = uL/L;
u4 = u2 - u3;
i0 = i1 + iL:
%
xdot = zeros(2, 1):
xdot(1) = du3; xdot(2) = diL;
%
return
```

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Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting XII

The model and output equations can be coded as follows:

function $[xdot] = st_eq2(x, t)$ R1 = 100; R2 = 100; R3 = 30;C = 1e - 6; L = 0.01;% $u^3 = x(1); \quad iL = x(2);$ % u0 = 10: i3 = u3/R3;i4 = 4 * u3;u1 = u0 - u3: i2 = i4 - iL: i1 = u1/R1; $u^2 = R^2 * i^2$: uL = u1 + u2;iC = i1 - i2 - i3du3 = iC/C: diL = uL/L;u4 = u2 - u3;i0 = i1 + iL: % xdot = zeros(2, 1);xdot(1) = du3; xdot(2) = diL;% return

function $[y] = \text{out}_{eq2}(x, t)$ R1 = 100; R2 = 100; R3 = 30;C = 1e - 6; L = 0.01;% $u^3 = x(1); \quad iL = x(2);$ % u0 = 10: i3 = u3/R3;i4 = 4 * u3: u1 = u0 - u3: $i^2 = i^4 - i^2$ i1 = u1/R1; $u^2 = R^2 * i^2$: uL = u1 + u2: iC = i1 - i2 - i3du3 = iC/C: diL = uL/L;u4 = u2 - u3;i0 = i1 + iL: % $y = \operatorname{zeros}(2, 1);$ $y(1) = u3; \quad y(2) = iC;$ % return ▲ロ ▶ ▲周 ▶ ▲ 国 ▶ ▲ 国 ▶ ● の Q @

Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting XIII

The simulation loop (with Gustafsson step-size control) can be coded as follows:

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```
while t < tf,
    [x4, x5] = rkf45\_step2(x, t, h);
    err = norm(x4 - x5, 'inf') / max([norm(x4), norm(x5), 1.0e - 10]);
    if err > tol,
        h = (0.8 * tol/err) \land (0.2) * h
        errl = 0:
    else
        t = t + h
        x = x5:
        y = \operatorname{out\_eq2}(x, t);
        tvec = [tvec, t];
        yvec = [yvec, y];
        if errl > 0,
             h = (0.8 * tol/err) \land (0.06) * (errl/err) \land (0.08) * h;
        else
             h = (0.8 * tol/err) \land (0.2) * h
        end
        hvec = [hvec, h]:
        errl = err:
    end
end
```

Homework 9 - Solution

Tarjan Algorithm

[H7.1] Electrical Circuit, Horizontal and Vertical Sorting XIV



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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity

Given the circuit shown below containing three sinusoidal current sources:

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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity

Given the circuit shown below containing three sinusoidal current sources:

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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity

Given the circuit shown below containing three sinusoidal current sources:



Write down the complete set of equations describing this circuit. Draw the structure digraph and begin causalizing the equations. Determine a constraint equation.

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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity

Given the circuit shown below containing three sinusoidal current sources:



- Write down the complete set of equations describing this circuit. Draw the structure digraph and begin causalizing the equations. Determine a constraint equation.
- Apply the Pantelides algorithm to reduce the perturbation index to 1. Then apply the tearing algorithm with substitution to bring the perturbation index down to 0.

Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity

Given the circuit shown below containing three sinusoidal current sources:



- Write down the complete set of equations describing this circuit. Draw the structure digraph and begin causalizing the equations. Determine a constraint equation.
- Apply the Pantelides algorithm to reduce the perturbation index to 1. Then apply the tearing algorithm with substitution to bring the perturbation index down to 0.
- Write down the structure incidence matrices of the index-1 DAE and the index-0 ODE systems, and show that they are in BLT form, and in LT form, respectively.

Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity II



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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity II



Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity III

1: 2: 3: 4: 5: 6: 7:	l ₁ l ₂ l ₃ u _R i _C u _{L1} u _{L2}		$f_{1}(t)$ $f_{2}(t)$ $f_{3}(t)$ $R \cdot i_{R}$ $C \cdot \frac{du_{C}}{dt}$ $L_{1} \cdot \frac{du_{L1}}{dt}$ $L_{2} \cdot \frac{du_{L2}}{dt}$
8: 9: 10: 11:	u ₁ u _{L1} u _{L2} u ₃	= = =	$u_{L1} + u_C$ $u_R + u_2$ $u_2 + u_C$ $u_R + u_{L2}$
12: 13: 14:	$I_1 + I_3$ I_{L1} I_{L2}	= = =	$i_{L1} + i_R$ $I_2 + i_C$ $I_2 + i_R$

Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity III

1: 2: 3: 4: 5: 6: 7:	l ₁ l ₂ l ₃ u _R i _C u _{L1} u _{L2}		$ \begin{array}{l} f_1(t) \\ f_2(t) \\ f_3(t) \\ R \cdot i_R \\ C \cdot \frac{du_C}{dt} \\ L_1 \cdot \frac{du_C}{dt} \\ L_2 \cdot \frac{du_2}{dt} \end{array} $
8: 9: 10: 11:	u ₁ u _{L1} u _{L2} u ₃		$u_{L1} + u_C$ $u_R + u_2$ $u_2 + u_C$ $u_R + u_{L2}$
12: 13: 14:	$I_1 + I_3$ I_{L1} I_{L2}	= = =	$i_{L1} + i_R$ $I_2 + i_C$ $I_2 + i_R$



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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity IV

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I causalized as much as I could without getting into trouble:



Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity IV

I causalized as much as I could without getting into trouble:



1:	<i>I</i> ₁	=	$f_1(t)$
2:	I_2	=	$f_2(t)$
3:	<i>I</i> ₃	=	$f_3(t)$
8:	u _R	=	R ∙ i _R
12:	i _C	=	$C \cdot \frac{du_C}{dt}$
11:	u _{L1}	=	$L_1 \cdot \frac{di_{L1}}{dt}$
10:	u _{L2}	=	$L_2 \cdot \frac{d\tilde{l}_{L^2}}{dt}$
14:	<i>u</i> ₁	=	$u_{L1} + u_{C}$
9:	u _{L1}	=	$u_{R} + u_{2}$
?:	u _{L2}	=	$u_2 + u_C$
13:	Из	=	$u_{R} + u_{L2}$
?:	$I_1 + I_3$	=	$i_{L1} + i_R$
4:	i _{L1}	=	$l_2 + i_C$
?:	i _{L2}	=	$l_{2} + i_{R}$

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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity V

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Any additional causalization leads invariably to a constraint:



Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity V

Any additional causalization leads invariably to a constraint:



1:	<i>I</i> ₁	=	$f_1(t)$
2:	I_2	=	$f_2(t)$
3:	<i>I</i> ₃	=	$f_3(t)$
8:	u _R	=	R ∙ i _R
12:	i _C	=	$C \cdot \frac{du_C}{dt}$
11:	u _{L1}	=	$L_1 \cdot \frac{di_{l1}}{dt}$
10:	u _{L2}	=	$L_2 \cdot \frac{di_{L_2}}{dt}$
14:	<i>u</i> 1	=	$u_{L1} + u_{C}$
9:	u _{L1}	=	$u_{R} + u_{2}$
?:	u _{L2}	=	$u_2 + u_C$
13:	Из	=	$u_R + u_{L2}$
5:	$I_1 + I_3$	=	$i_{L1} + i_R$
4:	i_{L1}	=	$l_{2} + i_{C}$
const.eq.:	i _{L2}	=	$I_2 + i_R$

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Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity VI

We differentiate the constraint equation and let go of the integrator for i_{L1} :

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1:	I_1	=	$f_1(t)$
2:	<i>I</i> ₂	=	$f_2(t)$
3:	<i>I</i> ₃	=	$f_3(t)$
8:	u _R	=	R ∙ i _R
12:	i _C	=	$C \cdot \frac{du_C}{dt}$
11:	u _{L1}	=	$L_1 \cdot \frac{di_{l1}}{dt}$
10:	u _{L2}	=	$L_2 \cdot \frac{dl_{L_2}}{dt}$
14:	<i>u</i> ₁	=	$u_{L1} + u_{C}$
9:	u _{L1}	=	$u_{R} + u_{2}$
?:	u _{L2}	=	<i>u</i> ₂ + <i>u</i> _C
13:	Из	=	$u_{R} + u_{L2}$
5:	$I_1 + I_3$	=	$i_{L1} + i_R$
4:	i_{L1}	=	$l_2 + i_C$
const.eq.:	i _{L2}	=	$I_2 + i_R$

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity VI

We differentiate the constraint equation and let go of the integrator for i_{L1} :

1:	I_1	=	$f_1(t)$	1:	I_1	=	$f_1(t)$
2:	I_2	=	$f_2(t)$	2:	I_2	=	$f_2(t)$
3:	<i>I</i> ₃	=	$f_3(t)$	3:	<i>I</i> ₃	=	$f_3(t)$
8:	u _R	=	R · i _R	10:	u _R	=	R ∙ i _R
12:	i _C	=	$C \cdot \frac{du_{C}}{dt}$ $L_{1} \cdot \frac{dL_{1}}{dt}$ $L_{2} \cdot \frac{dL_{2}}{dt}$	13:	i _C	=	$C \cdot \frac{du_C}{dt} \\ L_1 \cdot \frac{du_{L1}}{dt}$
11:	<i>u</i> _{L1}	=	$L_1 \cdot \frac{di_{L1}}{dt}$	12:	u _{L1}	=	$L_1 \cdot \frac{di_{L1}}{dt}$
10:	u _{L2}	=	$L_2 \cdot \frac{dl_{L_2}}{dt}$?:	u _{L2}	=	$L_2 \cdot di_{L2}$
14:	<i>u</i> ₁	=	$u_{L1} + u_C$	15:	<i>u</i> 1	=	$u_{L1} + u_{C}$
9:	u_{L1}	=	$u_R + u_2$	11:	<i>u</i> _{L1}	=	$u_{R} + u_{2}$
?:	u _{L2}	=	$u_2 + u_C$?:	u _{L2}	=	$u_2 + u_C$
13:	Из	=	$u_R + u_{L2}$	14:	Из	=	$u_{R} + u_{L2}$
5:	$I_1 + I_3$	=	$i_{L1} + i_R$	5:	$I_1 + I_3$	=	i _{L1} + i _R
4:	i _{L1}	=	$l_2 + i_C$	4:	i _{L1}	=	$l_2 + i_C$
const.eq.:	i_{L2}	=	$I_2 + i_R$	6:	i _{L2}	=	$I_2 + i_R$
				?:	di _{L2}	=	$dI_2 + di_R$

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity VII

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We introduced two new pseudo-derivatives, dl_2 and di_R :

1:	I_1	=	$f_1(t)$
2:	I_2	=	$f_2(t)$
3:	<i>I</i> ₃	=	$f_3(t)$
10:	u _R	=	R ∙ i _R
13:	i _C	=	$C \cdot \frac{du_C}{dt} \\ L_1 \cdot \frac{du_{L1}}{dt}$
12:	<i>u</i> _{L1}	=	$L_1 \cdot \frac{di_{l1}}{dt}$
?:	u _{L2}	=	$L_2 \cdot di_{L2}$
15:	<i>u</i> 1	=	$u_{L1} + u_{C}$
11:	<i>u</i> _{L1}	=	$u_{R} + u_{2}$
?:	u _{L2}	=	и ₂ + <mark>и</mark> С
14:	Из	=	$u_{R} + u_{L2}$
5:	$I_1 + I_3$	=	$i_{L1} + i_R$
4:	i _{L1}	=	$l_{2} + i_{C}$
6:	i _{L2}	=	$I_2 + i_R$
?:	di _{L2}	=	$dI_2 + di_R$

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity VII

We introduced two new pseudo-derivatives, dl_2 and di_R :

1:	<i>I</i> ₁	=	$f_1(t)$	1:	<i>I</i> ₁	=	$f_1(t)$
2:	<i>I</i> ₂	=	$f_2(t)$	2:	I 2	=	$f_2(t)$
3:	I ₃	=	$f_3(t)$	7:	dI_2	=	$\frac{df_2(t)}{dt}$
10:	u _R	=	R · i _R	3:	<i>I</i> ₃	=	$f_3(t)$
13:	i _C	=	$C \cdot \frac{du_C}{dt}$	8:	u _R	=	R · in
12:	u _{L1}	=	$C \cdot \frac{du_C}{dt} \\ L_1 \cdot \frac{du_{L1}}{dt}$	15:	i _C	=	$C \cdot \frac{du_C}{dt}$ $L_1 \cdot \frac{du_{L1}}{dt}$
?:	u _{L2}	=	$L_2 \cdot di_{L2}$?:	u_{L1}	=	$L_1 \cdot \frac{di_{L1}}{dt}$
15:	<i>u</i> ₁	=	$u_{L1} + u_C$?:	u _{L2}	=	$L_2 \cdot di_{L_2}$
11:	u _{L1}	=	$u_{R} + u_{2}$	17:	<i>u</i> ₁	=	$u_{L1} + u_{C}$
?:	u_{L2}	=	$u_2 + u_C$?:	u_{L1}	=	$u_R + u_2$
14:	u ₃	=	$u_{R} + u_{L2}$?:	u _{L2}	=	$u_2 + u_C$
5:	$I_1 + I_3$	=	$i_{L1} + i_R$	16:	U ₃	=	$u_R + u_{L2}$
4:	i _{L1}	=	$l_2 + i_C$	5:	$I_1 + I_3$	=	$i_{L1} + i_R$
6:	i _{L2}	=	$I_2 + i_R$?:	$dI_1 + dI_3$	=	$\frac{di_{L1}}{dt} + di$
?:	di _{L2}	=	$dI_2 + di_R$	4:	i _{L1}	=	$\frac{dt}{l_2} + \frac{i_C}{l_2}$
				6:	i_{L2}	=	$I_2 + i_R$
				2	·L2		·2 ·K

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= $dl_2 + di_R$ ▲日▼▲□▼▲□▼▲□▼ □ ○○○

 $L_2 \cdot di_{12}$ $u_{L1} + u_{C}$ $u_{R} + u_{2}$ $u_2 + u_C$ $u_{R} + u_{L2}$ $i_{L1} + i_R$ $\frac{di_{L1}}{dt} + di_R$

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity VIII

Two more pseudo-derivatives, dl_1 and dl_3 :

1:	<i>I</i> ₁	=	$f_1(t)$
2:	<i>I</i> ₂	=	$f_2(t)$
7:	dI_2	=	$\frac{df_2(t)}{dt}$
3:	<i>I</i> ₃	=	$f_3(t)$
8:	u _R	=	R · i _R
15:	i _C	=	$C \cdot \frac{du_C}{dt}$
?:	u_{L1}	=	$C \cdot \frac{dt}{dt}$ $L_1 \cdot \frac{dt_{l1}}{dt}$
?:	u _{L2}	=	$L_2 \cdot di_{L2}$
17:	<i>u</i> ₁	=	$u_{L1} + u_C$
?:	u_{L1}	=	$u_{R} + u_{2}$
?:	<i>u</i> _{L2}	=	<i>u</i> ₂ + <i>u</i> _C
16:	Из	=	$u_{R} + u_{L2}$
5:	$I_1 + I_3$	=	і _{L1} + і _R
?:	$dI_1 + dI_3$	=	$\frac{di_{L1}}{dt} + di_R$
4:	i _{L1}	=	$l_2 + i_C$
6:	i _{L2}	=	$I_2 + i_R$
?:	di _{L2}	=	$dl_2 + di_R$

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity VIII

Two more pseudo-derivatives, dI_1 and dI_3 :

1:	<i>I</i> ₁	=	$f_1(t)$
2:	I ₂	=	$f_2(t)$
7:	dI_2	=	$\frac{df_2(t)}{dt}$
3:	<i>I</i> 3	=	$f_3(t)$
8:	u _R	=	R · i _R
15:	i _C	=	$C \cdot \frac{du_C}{dt}$
?:	u_{L1}	=	$L_1 \cdot \frac{di_{L1}}{dt}$
?:	u _{L2}	=	$L_2 \cdot di_{L2}$
17:	<i>u</i> ₁	=	$u_{L1} + u_{C}$
?:	<i>u</i> _{L1}	=	$u_{R} + u_{2}$
?:	u _{L2}	=	$u_2 + u_C$
16:	U ₃	=	$u_{R} + u_{L2}$
5:	$I_1 + I_3$	=	$i_{L1} + i_R$
?:	$dI_1 + dI_3$	=	$\frac{di_{L1}}{dt} + di_R$
4:	<i>i</i> _{L1}	=	$l_2 + i_C$
6:	i _{L2}	=	$I_2 + i_R$
?:	di _{L2}	=	$dl_2 + di_R$

1:	<i>I</i> ₁	=	$f_1(t)$
9:	dI_1	=	$\frac{df_1(t)}{dt}$
2:	I_2	=	$f_2(t)$
7:	dI_2	=	$\frac{\frac{df_2(t)}{dt}}{f_3(t)}$
3:	<i>I</i> 3	=	$f_3(t)$
10:	dl ₃	=	$\frac{df_3(t)}{dt}$
8:	u _R	=	$R^{at} i_R$
17:	ic	=	$C \cdot \frac{du_C}{dt}$
?:	u_{L1}	=	$R \cdot i_{R}$ $C \cdot \frac{du_{C}}{dt}$ $L_{1} \cdot \frac{du_{L1}}{dt}$
?:	u _{L2}	=	$L_2 \cdot di_{L2}$
19:	<i>u</i> ₁	=	$u_{L1} + u_{C}$
?:	u_{L1}	=	$u_R + u_2$
?:	u _{L2}	=	$u_2 + u_C$
18:	u ₃	=	$u_R + u_{L2}$
5:	$I_1 + I_3$	=	$i_{L1} + i_R$
?:	$dI_1 + dI_3$	=	$\frac{di_{L1}}{dt} + di_R$
4:	i _{L1}	=	$l_2 + i_C$
6:	i _{L2}	=	$I_2 + i_R$
?:	di _{L2}	=	$dl_2 + di_R$
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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity IX

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	<i>I</i> 1	<i>I</i> ₂	<i>I</i> 3	ic	i _R	iL2	dl ₂	^u R	dl_1	dl ₃	^u L1	$\frac{di_{L1}}{dt}$	^u L2	di _{L2}	<i>u</i> 2	di _R	$\frac{du_C}{dt}$	u ₃	<i>u</i> ₁	
1:	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
2:	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
3:	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
4:	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
5:	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
6:	0	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	
7:	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	
8:	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	
9:	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	
10:	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	
11:	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	
12:	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	
13:	0	0	0	0	0	0	0	1	0	0	1	0	0	0	1	0	0	0	0	
14:	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	
15:	0	0	0	0	0	0	0	0	1	1	0	1	0	0	0	1	0	0	0	
16:	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	1	0	0	0	
17:	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	
18:	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	
19:	[0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1]	

Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity X

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We have an algebraic loop in six equations and six unknowns:

Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity X

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We have an algebraic loop in six equations and six unknowns:



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Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity X

We have an algebraic loop in six equations and six unknowns:



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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity X

We have an algebraic loop in six equations and six unknowns:





2:	<i>u</i> _{L1}	=	$L_1 \cdot \frac{dl_1}{dt}$
5:	u _{L2}	=	$L_2 \cdot di_{L2}$
3:	<i>u</i> _{L1}	=	$u_{R} + u_{2}$
4:	u _{L2}	=	$u_2 + u_C$
1:	$dI_1 + dI_3$	=	$\frac{di_{L1}}{dt} + di_R$
res.eq.:	di _{L2}	=	$d\bar{l}_2 + d\bar{l}_R$

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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity XI

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We have an algebraic loop in six equations and six unknowns:

2:	u _{L1}	=	$L_1 \cdot \frac{di_{l1}}{dt}$
5:	u _{L2}	=	$L_2 \cdot di_{L2}$
3:	<i>u</i> _{L1}	=	$u_{R} + u_{2}$
4:	u _{L2}	=	$u_2 + u_C$
1:	$dI_1 + dI_3$	=	$\frac{di_{L1}}{dt} + di_R$
res.eq.:	di _{L2}	=	$dI_2 + dI_R$

Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity XI

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We have an algebraic loop in six equations and six unknowns:

2:	u _{L1}	=	$L_1 \cdot \frac{di_{L1}}{dt}$
5:	и _{L2}	=	$L_2 \cdot di_{L2}$
3:	<i>u</i> _{L1}	=	<u>и_R + и2</u>
4:	u _{L2}	=	$u_2 + u_C$
1:	$dI_1 + dI_3$	=	$\frac{di_{L1}}{dt} + di_R$
res.eq.:	di _{L2}	=	$d\bar{l}_2 + d\bar{l}_R$

res.eq.:
$$di_R = di_{L2} - dl_2$$

1: $\frac{di_{L1}}{dt} = dl_1 + dl_3 - di_R$
2: $u_{L1} = L_1 \cdot \frac{di_{L1}}{dt}$
3: $u_2 = u_{L1} - u_R$
4: $u_{L2} = u_2 + u_C$
5: $di_{L2} = \frac{1}{L_5} \cdot u_{L2}$

Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity XI

We have an algebraic loop in six equations and six unknowns:

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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity XI

We have an algebraic loop in six equations and six unknowns:

Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity XII

1:
$$l_1 = f_1(t)$$

2: $l_2 = f_2(t)$
3: $l_3 = f_3(t)$
4: $i_C = i_{L1} - l_2$
5: $i_R = l_1 + l_3 - i_{L1}$
6: $i_{L2} = l_2 + i_R$
7: $dl_2 = \frac{df_2(t)}{dt}$
8: $u_R = R \cdot i_R$
9: $dl_1 = \frac{df_1(t)}{dt}$
10: $dl_3 = \frac{dg_3(t)}{dt}$
11: $di_R = \frac{l_1 \cdot (dl_1 + dl_3) - u_R + u_C - l_2 \cdot dl_2}{l_1 + l_2}$
12: $\frac{di_{11}}{dt} = dl_1 + dl_3 - di_R$
13: $u_{L1} = L_1 \cdot \frac{di_{L1}}{dt}$
14: $u_2 = u_{L1} - u_R$
15: $u_{L2} = u_2 + u_C$
16: $di_{L2} = \frac{1}{l_2} \cdot u_{L2}$
17: $\frac{du_C}{dt} = \frac{1}{C} \cdot i_C$
18: $u_3 = u_R + u_{L2}$
19: $u_1 = u_{L1} + u_C$

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Homework 9 - Solution

Pantelides Algorithm

[H7.7] Electrical Circuit, Structural Singularity XIII

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	<i>I</i> 1	<i>I</i> 2	13	ic	i _R	iL2	dl ₂	^u R	dl_1	dl3	di _R	$\frac{di_{L1}}{dt}$	^u L1	<i>u</i> 2	^u L2	di _{L2}	$\frac{du_C}{dt}$	u ₃	<i>u</i> ₁
1:	1	0	0	ō	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0]
2:	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
3:	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
4:	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
5:	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
6:	0	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
7:	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
8:	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0
9:	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
10:	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
11:	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0	0
12:	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
13:	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0
14:	0	0	0	0	0	0	0	1	0	0	0	0	1	1	0	0	0	0	0
15:	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0
16:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0
17:	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
18:	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
19:	[0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1]

Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm

The following set of DAEs:

$$\frac{dC}{dt} = K_1(C_0 - C) - R$$

$$\frac{dT}{dt} = K_1(T_0 - T) + K_2R - K_3(T - T_C)$$

$$0 = R - K_3 \exp\left(\frac{-K_4}{T}\right)C$$

$$0 = C - u$$

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describes a chemical isomerization reaction.

Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm

The following set of DAEs:

$$\frac{dC}{dt} = K_1(C_0 - C) - R$$

$$\frac{dT}{dt} = K_1(T_0 - T) + K_2R - K_3(T - T_C)$$

$$0 = R - K_3 \exp\left(\frac{-K_4}{T}\right)C$$

$$0 = C - u$$

describes a chemical isomerization reaction.

C is the reactant concentration, *T* is the reactant temperature, and *R* is the reactant rate per unit volume. C_0 is the feed reactant concentration, and T_0 is the feed reactant temperature. *u* is the desired concentration, and T_C is the control temperature that we need to produce *u*.

Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm II

• We want to turn the problem around (inverse model control) and determine the necessary control temperature T_C as a function of the desired concentration u. Thus, u will be an input to our model, and T_C is the output.

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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm II

- We want to turn the problem around (inverse model control) and determine the necessary control temperature T_C as a function of the desired concentration u. Thus, u will be an input to our model, and T_C is the output.
- Draw the structure digraph. You shall notice at once that one of the equations has no connections to it. Thus, it is a constraint equation that needs to be differentiated, while an integrator associated with the constraint equation needs to be thrown out.

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm II

- We want to turn the problem around (inverse model control) and determine the necessary control temperature T_C as a function of the desired concentration u. Thus, u will be an input to our model, and T_C is the output.
- Draw the structure digraph. You shall notice at once that one of the equations has no connections to it. Thus, it is a constraint equation that needs to be differentiated, while an integrator associated with the constraint equation needs to be thrown out.
- We now have five equations in five unknowns. Draw the enhanced structure digraph, and start causalizing the equations. You shall notice that a second constraint equation appears. Hence the original DAE system had been an index-3 DAE system. Differentiate that constraint equation as well, and throw out the second integrator. In the process, new pseudo-derivatives are introduced that call for additional differentiations.

Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm III

This time around, you end up with eight equations in eight unknowns. Draw the once more enhanced structure digraph, and causalize the equations. This is an example, in which (by accident) the Pantelides algorithm reduces the perturbation index in one step from 2 to 0, i.e., the final set of equations does not contain an algebraic loop.

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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm III

- This time around, you end up with eight equations in eight unknowns. Draw the once more enhanced structure digraph, and causalize the equations. This is an example, in which (by accident) the Pantelides algorithm reduces the perturbation index in one step from 2 to 0, i.e., the final set of equations does not contain an algebraic loop.
- ▶ Draw a block diagram that shows how the output T_C can be computed from the three inputs u, $\frac{du}{dt}$, and $\frac{d^2u}{dt^2}$.

Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm IV

The original equations are:

?:
$$\frac{dC}{dt} = K_1 \cdot (C_0 - C) - R$$

?:
$$\frac{dT}{dt} = K_1 \cdot (T_0 - T) + K_2 \cdot R - K_3 \cdot (T - T_C)$$

?:
$$0 = R - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot C$$

const.eq.:
$$0 = C - u$$

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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm IV

The original equations are:

?:
$$\frac{dC}{dt} = K_1 \cdot (C_0 - C) - R$$

?:
$$\frac{dT}{dt} = K_1 \cdot (T_0 - T) + K_2 \cdot R - K_3 \cdot (T - T_C)$$

?:
$$0 = R - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot C$$

const.eq.:
$$0 = C - u$$

With the structure digraph:



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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm IV

The original equations are:

?:
$$\frac{dC}{dt} = K_1 \cdot (C_0 - C) - R$$

?:
$$\frac{dT}{dt} = K_1 \cdot (T_0 - T) + K_2 \cdot R - K_3 \cdot (T - T_C)$$

?:
$$0 = R - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot C$$

const.eq.:
$$0 = C - u$$

With the structure digraph:



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We recognize immediately a constraint equation.

Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm V

The enhanced equations are:

?:
$$dC = K_1 \cdot (C_0 - C) - R$$

?: $\frac{dT}{dt} = K_1 \cdot (T_0 - T) + K_2 \cdot R - K_3 \cdot (T - T_C)$
?: $0 = R - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot C$
?: $0 = C - u$
?: $0 = dC - \frac{du}{dt}$

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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm V

The enhanced equations are:

?:
$$dC = K_1 \cdot (C_0 - C) - R$$

?: $\frac{dT}{dt} = K_1 \cdot (T_0 - T) + K_2 \cdot R - K_3 \cdot (T - T_C)$
?: $0 = R - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot C$
?: $0 = C - u$
?: $0 = dC - \frac{du}{dt}$

With the structure digraph:



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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm VI

We start coloring the structure digraph and recognize soon a second constraint equation:



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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm VI

We start coloring the structure digraph and recognize soon a second constraint equation:



?:
$$dC = K_1 \cdot (C_0 - C) - R$$

?: $\frac{dT}{dt} = K_1 \cdot (T_0 - T) + K_2 \cdot R - K_3 \cdot (T - T_C)$
?: $0 = R - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot C$
?: $0 = C - u$
?: $0 = dC - \frac{du}{dt}$

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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm VII

The once more enhanced equations are:

?:
$$dC = K_1 \cdot (C_0 - C) - R$$

?: $dT = K_1 \cdot (T_0 - T) + K_2 \cdot R - K_3 \cdot (T - T_C)$
?: $0 = R - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot C$
?: $0 = C - u$
?: $0 = dC - \frac{du}{dt}$
?: $d2C = K_1 \cdot (\frac{dC_0}{dt} - dC) - dR$
?: $0 = dR - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot \left[dC + \frac{K_4 \cdot C \cdot dT}{T^2}\right]$
?: $0 = d2C - \frac{d^2u}{dt^2}$

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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm VIII

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Let us color the structure digraph:



Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm VIII

Let us color the structure digraph:





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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm VIII

Let us color the structure digraph:



We went from index-2 directly down to index-0. This sometimes happens.

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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm IX

4:
$$dC = K_1 \cdot (C_0 - C) - R$$

8: $dT = K_1 \cdot (T_0 - T) + K_2 \cdot R - K_3 \cdot (T - T_C)$
6: $0 = R - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot C$
1: $0 = C - u$
2: $0 = dC - \frac{du}{dt}$
5: $d2C = K_1 \cdot (\frac{dC_0}{dt} - dC) - dR$
7: $0 = dR - K_3 \cdot \exp\left(\frac{-K_4}{T}\right) \cdot \left[dC + \frac{K_4 \cdot C \cdot dT}{T^2}\right]$
3: $0 = d2C - \frac{d^2u}{dt^2}$

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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm X

1:
$$C = u$$

2: $dC = \frac{du}{dt}$
3: $d2C = \frac{d^2u}{dt^2}$
4: $R = K_1 \cdot (C_0 - C) - dC$
5: $dR = K_1 \cdot (\frac{dC_0}{dt} - dC) - d2C$
6: $T = \frac{-K_4}{\log(\frac{R}{3} \cdot C)}$
7: $dT = \frac{T^2}{K_3 \cdot K_4 \cdot C} \cdot \left[dR \cdot \exp(\frac{K_4}{T}) - K_3 \cdot dC \right]$
8: $T_C = \frac{dT - K_1 \cdot (T_0 - T) - K_2 \cdot R + K_3 \cdot T}{K_3}$

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Homework 9 - Solution

Pantelides Algorithm

[H7.8] Chemical Reactions, Pantelides Algorithm XI



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