THE EFFECTS OF IONIZING RADIATION ON POWER-MOSFET TERMINATION STRUCTURES^{*}

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ABSTRACT

The effects of ionizing radiation on power-MOSFET termination structures were examined through two-dimensional simulation. A wide range of sensitivity to surface-charge density was found for various devices employing floating field rings and/or equipotential field plates. Termination structures that were both insensitive to surface charge and possessed a high breakdown voltage were identified. The results were compared with measurements made on selected structures.

I. INTRODUCTION

The maximum breakdown voltage obtainable for a p-n junction of specified doping concentration occurs for a planar structure [1]. In actual devices utilizing planar diffusion technology, however, the high-voltage junction must intersect the surface at some finite position. The resulting junction curvature compresses the equipotential lines where the junction bends to the surface and increases the peak electric field. Junction-termination structures are used in power devices to reduce the peak electric field and to allow the breakdown voltage to approach its ideal one-dimensional value. The most commonly used termination methods are field rings and field plates.

Avalanche multiplication is the physical mechanism that causes the primary breakdown of high-voltage reverse-biased p-n junctions. Breakdown occurs when the electric field in the junction depletion region increases to the point at which the impact ionization rate approaches infinity. The most important factor that influences the magnitude and location of the peak electric field in planar semiconductor technology is the method used to terminate the junction [1-3]. The objective of the different termination methods is to reduce the peak electric field either at the surface or at the curved junction regions in the bulk.

The drain-source breakdown voltage of power MOSFETs

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is strongly affected by ionizing radiation [4, 5]. This is a result of the introduction of trapped oxide and interface charge in the field oxide. These trapped charges alter the potential at the surface of the junction and, in turn, become a part of the junction termination. Therefore, radiation-induced charge affects the breakdown voltage of high-voltage junctions.

This paper reports on work designed to examine the effects of ionizing radiation on the most commonly used power-MOSFET termination structures. Simulations were conducted using computer code specifically designed for this purpose. The results are presented and verified with measurements made on selected structures. Insights into the design of optimum termination structures are obtained.

II. HIGH-VOLTAGE TERMINATION TECHNIQUES

Junction curvature significantly reduces the breakdown voltage of p-n junctions because of the increase of the electric field in the curved area [1]. This curvature is dependent on the junction depth, the ratio of the radius of curvature to depletion width, and the relative doping concentration. High-voltage devices rely on the minimization of this curvature through the use of appropriate junction termination structures.

A. Floating Field Rings

Figure 1 shows the cross-section of a power-MOSFET which includes one floating field ring. Field rings are often used to reduce the electric-field magnitude in the vicinity of the curved regions of the junction. The structure is designed



Fig. 1 Cross-section of power MOSFET with one floating field ring.

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to allow the reverse-biased junction's depletion region to punch-through to the ring well before the critical electric field for breakdown is attained. The effectiveness of the floating field ring is the greatest for high-voltage devices fabricated with shallow junctions [3]. When no surface charge is present, punch-through occurs at the surface. With the addition of surface charge, the depletion region may be compressed at the surface. In this case, for the field ring to be effective, punch-through must take place in the bulk silicon before the surface electric field becomes so high that breakdown occurs.

Optimally placing the field ring can result in a doubling of the breakdown voltage [7]. This optimal spacing depends on the oxide thickness and doping concentration. Adding surface charge in N-channel power-MOSFETs decreases the breakdown voltage [4, 5]. When the surface charge is fixed, it is possible to determine the optimal location of the field ring. When the surface charge is varied, however, the optimal location of the field ring changes, also.

B. Equipotential Field Plates

A field plate is a conductor placed above, and insulated from, the junction. It is usually connected to one side of the junction and is an extension of the junction metallization over the oxide as shown in Fig. 2. A field plate alters the surface potential by acting through the oxide layer to force the depletion layer at the surface to extend beyond the edge of the field plate. This "stretching out" of the depletion region reduces the depletion layer curvature and, in turn, reduces the peak electric field. It is common to use a field plate in conjunction with floating field rings by placing the plate on the main junction with floating rings beyond the junction or using a field plate on the last floating ring as shown in Fig. 3.



Fig. 2 Field-plate termination structure (After Baliga [3]).

III. METHOD OF SIMULATION

Computer code designed to analyze breakdown phenomena in high-voltage devices [6] has been modified to



Fig. 3 Termination structure using combination of field rings and field plates. (After Baliga [3]).

simulate the effects of ionizing radiation on the breakdown voltage of power-MOSFETs. The code, entitled ASEPS (Arizona SEmiconductor Power device Simulator), solves Poisson's equation for the electrostatic potential distribution in two dimensions for an arbitrary device, subject to appropriate boundary conditions. Unlike many general-purpose simulators, ASEPS neglects the contribution of mobile charge to Poisson's equation. This approximation is very accurate for strongly reverse-biased junctions. This simplification results in reduced simulation time and improved convergence of the solution, without significant sacrifice in accuracy. Average simulation times on a Digital Equipment Corporation MicroVAX 3600 range from 5 minutes for simple structures to 25 minutes for more complex structures. ASEPS interfaces with AutoCAD [13] to produce detailed drawings of the potential distribution and electric field distribution suitable for analysis.

The radiation-induced areal surface-charge density, N_{ss} , used in the difference equations is an input parameter to the ASEPS code. In this work, N_{ss} represents the algebraic difference between the positive oxide trapped charge (N_{ot}) and the negative interface trapped charge (N_{it}) and is taken to be positive. ASEPS draws a rectangular box around each node of the discretized domain of the structure being simulated, and this surface-charge density is only included in the difference equations for those nodes at the surface. A 200 × 200 grid is practical for describing the simulation domain of the structure. The Newton successive overrelaxation method of solving the resulting nonlinear equations is employed until convergence of a solution is reached.

ASEPS uses the ionization integral,

$$1 - \frac{1}{M_p} = \int_0^x \alpha_p \, exp\left[\int_0^{x'} (\alpha_n - \alpha_p) \, dx'' \right] dx' \qquad (1)$$

to calculate the multiplication factor for determination of avalanche breakdown [7]. In Eq. 1, M_p is the multiplication factor for holes, α_p is the ionization rate for holes, α_n is the ionization rate for electrons, and x is the depletion-layer

width of the ionization path. The ionization path is determined by starting from the peak electric field location and following the potential gradient line in both directions [8]. Ionization rates are from Sze and Gibbons [9]. Breakdown was assumed to occur when the multiplication factor for holes, $M_{\rm p}$, reached the value of 10 [10].

IV. RESULTS

For a one-dimensional abrupt junction, breakdown voltage is approximated by the following equation [7],

$$V_{BD} = \frac{\varepsilon E_{max}^2}{2 q N_{BC}},$$
 (2)

where ε is the permittivity, E_{max} is the maximum electric field, q is the elementary charge, and N_{BC} is the background (substrate) doping concentration. A one-dimensional structure was simulated using ASEPS. The doping profile used was approximately Gaussian with a surface concentration of 1×10^{18} cm⁻³. The substrate concentration was approximately 0.9×10^{15} cm⁻³. This doping profile was obtained from spreading resistance measurements made on an existent device. ASEPS calculated the breakdown voltage to be 327 V with an E_{max} of 3.0×10^5 V/cm, while Eq. (2) predicts that the breakdown voltage will be 324 V. Thus for this simple example, there is less than a 1% difference between Eq. (2) and simulation.

A simple *p*-*n* junction formed by diffusion is considered next. Fig. 4a shows a cross section of this structure with equipotential lines calculated by ASEPS superimposed. The junction depth is 5 μ m, the oxide thickness is 0.7 μ m, N_{ss} is zero, and the doping profile described above is used. The solid circle in Fig. 4a shows the location of the peak electric field. The value of the field is 3.5×10^5 V/cm. Calculation of the multiplication factor along the ionization path indicated that breakdown occurred for a reverse-bias voltage of 194 V. 194 V is only 59% of the optimal 327 V that might be achieved in an ideal junction with $N_{RC} = 0.9 \times 10^{15}$ cm⁻³.

Fig. 4b shows the same cross section except that the device was simulated with $N_{ss} = 1 \times 10^{12}$ charges/cm². Because the net charge in the oxide has been taken to be positive, the carrier density in the *n* region near the surface increases. This causes a decrease in depletion layer width and the crowding of the potential lines at the surface. The solid circle, again, shows the peak electric field location, which has moved to the surface. This location of the peak electric field results in a drastic reduction in breakdown voltage. A plot of breakdown voltage versus surface-charge density for this device can be found as the curve labeled "0 Rings" in Fig. 8.

The first technique considered to improve the breakdown voltage is the addition of one floating field ring optimally spaced from the main junction. This device, with the potential



Fig. 4 Potential distribution for simple two-dimensional *p*-*n* junction. (a) $N_{ss} = 0$, $V_{BD} = 194$ V. (b) $N_{ss} = 10^{12}$ cm⁻², $V_{BD} = 92$ V.

distribution for $N_{ss} = 0$, is shown in Fig. 5 where optimal spacing is achieved because the high electric field spots (circles) appear on both junction curvatures simultaneously indicating that both junctions broke down. From the computer simulations, it was verified that this spacing resulted in the highest breakdown voltage. This optimal spacing was 9.5 μ m



Fig. 5 Potential distribution for one-ring termination structure ($N_{ss} = 0$, $V_{BD} = 250$ V).

between rings after lateral diffusion. Figure 8, where breakdown voltage of this one-ring device is plotted versus surface charge, indicates that for $N_{ss} = 9 \times 10^{11}$ cm⁻² or greater, the breakdown curve followed that of the zero-ring device. This happened because punch-through to the field ring did not take place before breakdown occurred at this value of N_{ss} . Better results were obtained for a three-ring device. Again, the sharp bends in the curve plotted in Fig. 8 are a result of the punch-through phenomenon not reaching a ring before breakdown occurred. The spacing of the rings was kept fixed in determining the optimal spacing in this three-ring device. It was found that this distance was 7.5 μ m and this device structure is shown in Fig. 6, with the overlaid potential distribution simulated for $N_{ss} = 0$.



Fig. 6 Potential distribution for three-ring termination structure (N_{ss} = 0, V_{BD} = 278 V).

Variable ring spacing increases the breakdown voltage of devices having more than one ring. For avalanche breakdown to occur on all rings simultaneously, the spacing between different pairs of rings must be non-constant. The seven-ring breakdown curve in Fig. 8 is for a device with this type of spacing. Fig. 7 shows this device, with the potential distribution for $N_{ss} = 0$, where the spacing between the first ring and the main junction is 2.0 μ m, increasing to 12.0 μ m between the last pair of rings. Experimental measurements on this device indicated no degradation of the 268 V breakdown voltage for total-dose levels up to 1 MRad(Si). ASEPS calcu-

lated the breakdown voltage to be 281 V which is in error by only 5%. From the potential distribution in Fig. 7, it can be seen that the depletion layer depth below each ring gradually decreases as one moves farther from the main junction. The potential difference between a ring and the bulk material decreases as the distance of the ring from the main junction increases. This gradual decrease in depletion layer depth causes the termination to approximate the performance of a parallel-plane junction.

The measured breakdown voltage of a six-ring structure is plotted versus surface-charge density in Fig. 9. Each data point represents the average of six samples. The total dose has been converted to surface-charge density using the formula [12],

$$N_{\rm ss} = D t_{\rm ox} G F \tag{3}$$

where D is the total dose, t_{ox} is the oxide thickness, G is the generation rate of electron-hole pairs per unit volume, and F is the fraction of holes transported and trapped at the interface. An appropriate value of F was used in this simple model (F = 0.001) to calibrate the experimental data to the simulation. A more sophisticated model or additional experiments could be used to establish more precisely the relation between total dose and surface-charge density if required. In most situations, however, it is sufficient to minimize the dependence of breakdown voltage on surface-charge density.

The field plate/field ring combination shown in Fig. 10a had an initial breakdown voltage that was higher than that of a one-ring structure, but the advantage disappeared with increased surface charge. This is due to the fact that punch-through does not occur for higher charge densities and the plate has no effect (Fig. 10b). These results are presented in Fig. 11 where a comparison of this plate structure is made with the one-ring structure that does not use a field plate.



Fig. 7 Potential distribution for seven-ring termination structure ($N_{ss} = 0, V_{BD} = 281 \text{ V}$).



Fig. 8 Breakdown voltage vs. surface-charge density for various numbers of floating rings.

V. CONCLUSIONS

The breakdown behavior of typical power-MOSFET termination structures was examined as a function of radiation-induced surface charge, and the results for selected structures were verified experimentally. The specific results presented are a function of doping profile and oxide thickness, but the approach is readily applicable to structures with other values of these parameters. A wide range of sensitivity to radiation-induced charge was found for different termination structures.

For a field plate to be effective in increasing the breakdown voltage, it is necessary to use an oxide thickness sufficiently large so that the depletion layer curvature is reduced and the peak electric field is moved lower in the semiconductor bulk. With a large oxide thickness, though, the structure is very sensitive to surface charge. This causes field-plate terminations to degrade severely in radiation environments because field plates modify the potential distribution in the semiconductor by acting through an intervening dielectric layer. Any charge that is added in this dielectric layer produces a significant change in the potential distribution in the semiconductor.



Fig. 9 Breakdown voltage vs. surface-charge density for a sixring structure. Each data point represents the average of six samples. The line represents simulation results.

To achieve a high breakdown voltage and low sensitivity to surface charge, a device employing multiple field rings was found to work well. With just one ring, the breakdown voltage was substantially increased and sensitivity to surface charge was reduced. The optimal spacing of this ring was found to be 9.5 μ m such that the breakdown voltage was maximized and the peak electric field minimized for the given impurity profile. The optimal spacing of a three-ring device was 7.5 μ m between rings spaced uniformly. For avalanche multiplication to occur on all rings simultaneously, the spacing of the rings should gradually increase as distance from the main junction increases. This method of spacing the rings was used in the seven-ring device where the first ring was spaced 2.0 μ m from the main junction.





Fig. 10 Potential distribution for combination ring/plate structure. (a) $N_{ss} = 0$, $V_{BD} = 270$ V. (b) $N_{ss} = 10^{12}$ cm⁻³, $V_{BD} = 92$ V.

The trend is that as the number of rings in the device is increased, the spacing between these rings should steadily decrease. The best results are obtained for devices with a large number of rings and very little spacing between them.

There is, however, a tradeoff between die area and the desired increase in breakdown voltage and the desired insensitivity to surface charge. The increase in breakdown voltage of an eight-ring device over a seven-ring device is very slight. The most appropriate number of rings will be de-

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Fig. 11 Breakdown voltage vs. surface charge density for a one-ring structure and a one-ring structure with a field plate.

termined by the application and by the radiation environment in which the device is to be deployed.

Great care must be taken when designing a power MOSFET for use in an ionizing radiation environment. ASEPS has proven to be a valuable tool for this type of design work [14]. Of the structures examined, multiple floating rings had the least sensitivity to radiation-induced charge. The spacing between these rings is critical because radiationinduced charge can render the outer rings ineffective if the distance between them is too large. By using ASEPS with some initial insight as to what the spacing should be, one has a good starting point for determining optimal ring spacing.

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