

# How to Write Fast Numerical Code

Spring 2014

*Lecture:* Spiral (Computer generation of FFT code)

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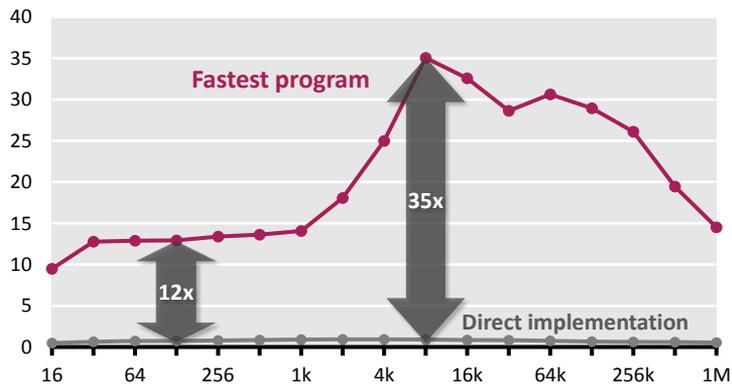
**ETH**

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## The Problem: Example DFT

DFT on Intel Core i7 (4 Cores, 2.66 GHz)

Performance [Gflop/s]

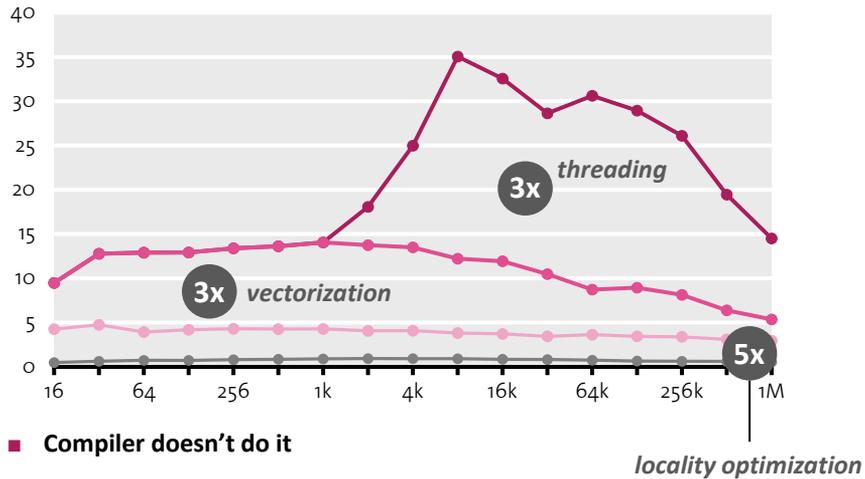


- Same number of operations
- Best compiler

## DFT: Analysis

DFT (single precision) on Intel Core i7 (4 cores, 2.66 GHz)

Performance [Gflop/s]



## Our Goal:

Computer writes high performance library code

Generate Code



"click"

**Select convolutional code**  
Select a preset code or customize parameters

- custom
- Voyager
- NASA-DSN
- CCSDS/NASA-GSFC
- WiMax
- CDMA IS-95A
- LTE (3GPP - Long Term Evolution)
- UWB (802.15)
- CDMA 2000
- Cassini
- Mars Pathfinder & Stereo

rate  /  code rate [\(?\)](#)

K  constraint length [\(?\)](#)

polynomials  polynomials for the code in decimal notation [\(?\)](#)

**Select implementation options**

frame length  unpadded frame length

Vectorization level  type of code [\(?\)](#)

## DFT IP Cores

parameter	value	range	explanation
<b>Problem specification</b>			
transform size	<input type="text" value="64"/>	4-32768	Number of samples <a href="#">(?)</a>
direction	<input type="text" value="forward"/>		forward or inverse DFT <a href="#">(?)</a>
data type	<input type="text" value="fixed point"/>		fixed or floating point <a href="#">(?)</a>
	<input type="text" value="16"/> bits	4-32 bits	fixed point precision <a href="#">(?)</a>
	<input type="text" value="unscaled"/>		scaling mode <a href="#">(?)</a>
<b>Parameters controlling implementation</b>			
architecture	<input type="text" value="fully streaming"/>		iterative or fully streaming <a href="#">(?)</a>
radix	<input type="text" value="2"/>	2, 4, 8, 16, 32, 64	size of DFT basic block <a href="#">(?)</a>
streaming width	<input type="text" value="2"/>	2-64	number of complex words per cycle <a href="#">(?)</a>
data ordering	<input type="text" value="natural in / natural out"/>		natural or digit-reversed data order <a href="#">(?)</a>
BRAM budget	<input type="text" value="1000"/>		maximum # of BRAMs to utilize (-1 for no limit) <a href="#">(?)</a>

**Viterbi Decoder**

[@ www.spiral.net](http://www.spiral.net)

## Possible Approach:

Capturing algorithm knowledge:  
**Domain-specific languages (DSLs)**

Structural optimization:  
**Rewriting systems**

High performance code style:  
**Compiler**

Decision making for choices:  
**Machine learning**

## Organization

- *Spiral: Basic system*
- Vectorization
- General input size
- Results
- Final remarks

## Algorithms: Example FFT, n = 4

### *Fast Fourier transform (FFT)*

$$\begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & i & -1 & -i \\ 1 & -1 & 1 & -1 \\ 1 & -i & -1 & i \end{bmatrix} x = \begin{bmatrix} 1 & \cdot & 1 & \cdot \\ \cdot & 1 & \cdot & 1 \\ 1 & \cdot & -1 & \cdot \\ \cdot & 1 & \cdot & -1 \end{bmatrix} \begin{bmatrix} 1 & \cdot & \cdot & \cdot \\ \cdot & 1 & \cdot & \cdot \\ \cdot & \cdot & 1 & \cdot \\ \cdot & \cdot & \cdot & i \end{bmatrix} \begin{bmatrix} 1 & 1 & \cdot & \cdot \\ 1 & -1 & \cdot & \cdot \\ \cdot & \cdot & 1 & 1 \\ \cdot & \cdot & 1 & -1 \end{bmatrix} \begin{bmatrix} 1 & \cdot & \cdot & \cdot \\ \cdot & \cdot & 1 & \cdot \\ \cdot & 1 & \cdot & \cdot \\ \cdot & \cdot & \cdot & 1 \end{bmatrix} x$$

### *Representation using matrix algebra*

$$\text{DFT}_4 = (\text{DFT}_2 \otimes \text{I}_2) \text{T}_2^4 (\text{I}_2 \otimes \text{DFT}_2) \text{L}_2^4$$

- *SPL (Signal processing language):* Mathematical, declarative, point-free
- Divide-and-conquer algorithms = breakdown rules in SPL



# Program Generation in Spiral

Transform  $DFT_8$

**Decomposition rules**

Algorithm (SPL)  
 $(DFT_2 \otimes I_4) T_4^8 (I_2 \otimes ((DFT_2 \otimes I_2) T_2^4 (I_2 \otimes DFT_2) L_2^4)) L_2^8$

parallelization  
vectorization

Algorithm ( $\Sigma$ -SPL)  
 $\sum (S_j DFT_2 G_j) \sum (\sum (S_{k,l} \text{diag}(t_{k,l}) DFT_2 G_l) \sum (S_m \text{diag}(t_m) DFT_2 G_{k,m}))$

locality  
optimization

C Program

```
void sub(double *y, double *x) {
double f0, f1, f2, f3, f4, f7, f8, f10, f11;
f0 = x[0] - x[3];
f1 = x[0] + x[3];
f2 = x[1] - x[2];
f3 = x[1] + x[2];
f4 = f1 - f3;
y[0] = f1 + f3;
y[2] = 0.7071067811865476 * f4;
f7 = 0.9238795325112867 * f0;
< more lines >
```

basic block  
optimizations

+ Search or  
Learning

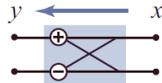
## Organization

- Spiral: Basic system
- **Vectorization**
- General input size
- Results
- Final remarks

## Example: Vectorization in Spiral

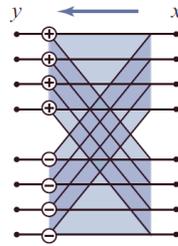
- Relationship SPL expressions  $\leftrightarrow$  vectorization?

$$y = \text{DFT}_2 x$$



one addition  
one subtraction

$$y = \begin{pmatrix} \text{DFT}_2 & I_4 \end{pmatrix} x$$



one (4-way) vector addition  
one (4-way) vector subtraction

## Step 1: Identify “Good” Vector Constructs

- Vector length:  $\nu$
- Good (= easily vectorizable) SPL constructs:

$$A \quad I_\nu$$

$$L_\nu^{\nu^2}, L_2^{2\nu}, L_\nu^{2\nu} \quad \text{base cases}$$

SPL expressions recursively built from those

- Idea:** Convert a given SPL expression into a “good” SPL expression through rewriting (structural manipulation)

## Step 2: Find Manipulation Rules

$$\begin{aligned}
 \mathbb{L}_n^{n\nu} &\rightarrow (\mathbb{I}_{n/\nu} \quad \mathbb{L}_\nu^{2\nu}) (\mathbb{L}_{n/\nu}^n \quad \mathbb{I}_\nu) \\
 \mathbb{L}_\nu^{n\nu} &\rightarrow (\mathbb{L}_\nu^n \quad \mathbb{I}_\nu) (\mathbb{I}_{n/\nu} \quad \mathbb{L}_\nu^{2\nu}) \\
 \mathbb{L}_m^{mn} &\rightarrow (\mathbb{L}_m^{mn/\nu} \quad \mathbb{I}_\nu) (\mathbb{I}_{mn/\nu^2} \quad \mathbb{L}_\nu^{2\nu}) (\mathbb{I}_{n/\nu} \quad \mathbb{L}_{m/\nu}^m \quad \mathbb{I}_\nu) \\
 \mathbb{I}_l \quad \mathbb{L}_n^{kmn} \quad \mathbb{I}_r &\rightarrow (\mathbb{I}_l \quad \mathbb{L}_n^{kn} \quad \mathbb{I}_{mr}) (\mathbb{I}_{kl} \quad \mathbb{L}_n^{mn} \quad \mathbb{I}_r) \\
 \mathbb{I}_l \quad \mathbb{L}_n^{kmn} \quad \mathbb{I}_r &\rightarrow (\mathbb{I}_l \quad \mathbb{L}_{kn}^{kmn} \quad \mathbb{I}_r) (\mathbb{I}_l \quad \mathbb{L}_{mn}^{kmn} \quad \mathbb{I}_r) \\
 \mathbb{I}_l \quad \mathbb{L}_{km}^{kmn} \quad \mathbb{I}_r &\rightarrow (\mathbb{I}_{kl} \quad \mathbb{L}_{mn}^{kmn} \quad \mathbb{I}_r) (\mathbb{I}_l \quad \mathbb{L}_k^{kmn} \quad \mathbb{I}_{mr})
 \end{aligned}$$

**Manipulation rules = Processor knowledge in Spiral**

$$\begin{aligned}
 (\mathbb{I}_m \quad \mathbb{A}^{n \times n}) \mathbb{L}_m^{mn} &\rightarrow (\mathbb{I}_{m/\nu} \quad \mathbb{L}_\nu^{2\nu} (\mathbb{A}^{n \times n} \quad \mathbb{I}_\nu)) (\mathbb{L}_{m/\nu}^{mn/\nu} \quad \mathbb{I}_\nu) \\
 \mathbb{L}_n^{mn} (\mathbb{I}_m \quad \mathbb{A}^{n \times n}) &\rightarrow (\mathbb{L}_n^{mn/\nu} \quad \mathbb{I}_\nu) (\mathbb{I}_{m/\nu} \quad (\mathbb{A}^{n \times n} \quad \mathbb{I}_\nu) \mathbb{L}_\nu^{n\nu}) \\
 (\mathbb{I}_k \quad (\mathbb{I}_m \quad \mathbb{A}^{n \times n}) \mathbb{L}_m^{mn}) \mathbb{L}_k^{kmn} &\rightarrow (\mathbb{L}_k^{km} \quad \mathbb{I}_n) (\mathbb{I}_m \quad (\mathbb{I}_k \quad \mathbb{A}^{n \times n}) \mathbb{L}_k^{kn}) (\mathbb{L}_m^{mn} \quad \mathbb{I}_k) \\
 \mathbb{L}_{mn}^{kmn} (\mathbb{I}_k \quad \mathbb{L}_n^{mn} (\mathbb{I}_m \quad \mathbb{A}^{n \times n})) &\rightarrow (\mathbb{L}_n^{mn} \quad \mathbb{I}_k) (\mathbb{I}_m \quad \mathbb{L}_n^{kn} (\mathbb{I}_k \quad \mathbb{A}^{n \times n})) (\mathbb{L}_m^{km} \quad \mathbb{I}_n) \\
 \overline{AB} &\rightarrow \overline{AB} \\
 \frac{\overline{A^{m \times m}}}{\mathbb{I}_\nu} &\rightarrow (\mathbb{I}_m \quad \mathbb{L}_\nu^{2\nu}) (\overline{A^{m \times m}} \quad \mathbb{I}_\nu) (\mathbb{I}_m \quad \mathbb{L}_\nu^{2\nu}) \\
 \frac{\mathbb{I}_m \quad \mathbb{A}^{n \times n}}{\mathbb{I}_\nu} &\rightarrow \mathbb{I}_m \quad \frac{\mathbb{A}^{n \times n}}{\mathbb{I}_\nu} \\
 \overline{D} &\rightarrow (\mathbb{I}_{n/\nu} \quad \mathbb{L}_\nu^{2\nu}) \overline{D} (\mathbb{I}_{n/\nu} \quad \mathbb{L}_\nu^{2\nu}) \\
 \overline{P} &\rightarrow \overline{P} \quad \mathbb{I}_2
 \end{aligned}$$

## Example

$$\begin{aligned}
 \frac{\overline{\text{DFT}_{mn}}}{\text{vec}(\nu)} &\rightarrow \frac{(\overline{\text{DFT}_m} \quad \mathbb{I}_n) \mathbb{T}_n^{mn} (\mathbb{I}_m \quad \overline{\text{DFT}_n}) \mathbb{L}_m^{mn}}{\text{vec}(\nu)} \\
 &\dots \\
 &\dots \\
 &\dots \\
 &\rightarrow \frac{(\mathbb{I}_{mn/\nu} \quad \mathbb{L}_\nu^{2\nu}) (\overline{\text{DFT}_m} \quad \mathbb{I}_n \quad \mathbb{I}_\nu) \mathbb{T}_n^{mn}}{(\mathbb{I}_m \quad \mathbb{L}_\nu^{2\nu} \quad \mathbb{I}_\nu) (\mathbb{I}_{2n/\nu} \quad \mathbb{L}_\nu^{2\nu}) (\mathbb{I}_n \quad \mathbb{L}_\nu^{2\nu} \quad \mathbb{I}_\nu) (\overline{\text{DFT}_n} \quad \mathbb{I}_\nu) (\mathbb{L}_{m/\nu}^{mn} \quad \mathbb{L}_\nu^{2\nu})}
 \end{aligned}$$

**vectorized arithmetic**  
**vectorized data accesses**

# Automatically Generate Base Case Library

- **Goal:** Given instruction set, generate base cases

$$\nu = 4 : \{ L_2^4, I_2, L_2^4, L_2^4, I_2, L_2^8, L_4^8 \}$$

- **Idea:** Instructions as matrices + search

$$y = \text{\_mm\_unpacklo\_ps}(x0, x1);$$

$$y = \text{\_mm\_shuffle\_ps}(x0, x1, \text{\_MM\_SHUFFLE}(1,2,1,2));$$

$$y = \text{\_mm\_shuffle\_ps}(x0, x1, \text{\_MM\_SHUFFLE}(3,4,3,4));$$

$$y = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} x_0 \\ x_1 \end{bmatrix}$$

$$y = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \end{bmatrix} \begin{bmatrix} x_0 \\ x_1 \end{bmatrix}$$

$$y = \begin{bmatrix} 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} x_0 \\ x_1 \end{bmatrix}$$

$$\begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

$$y0 = \text{\_mm\_shuffle\_ps}(x0, x1, \text{\_MM\_SHUFFLE}(1,2,1,2));$$

$$y1 = \text{\_mm\_shuffle\_ps}(x0, x1, \text{\_MM\_SHUFFLE}(3,4,3,4));$$



# Same Approach for Different Paradigms

## Threading:

$$\text{DFT}_{\text{smp}(\nu, \mu)}^{\text{mm}} \rightarrow \frac{(\text{DFT}_m \otimes I_n) T_n^{\text{mm}} (I_m \otimes \text{DFT}_n) L_m^{\text{mm}}}{\text{smp}(\nu, \mu)}$$

$$\dots$$

$$\rightarrow \frac{(\text{DFT}_m \otimes I_n) T_n^{\text{mm}} (I_m \otimes \text{DFT}_n) L_m^{\text{mm}}}{\text{smp}(\nu, \mu) \text{smp}(\nu, \mu) \text{smp}(\nu, \mu) \text{smp}(\nu, \mu)}$$

$$\dots$$

$$\rightarrow \left( (L_m^{\text{mm}} \otimes I_{n/\nu}) \otimes_{\mu} I_{\nu} \right) \left( I_p \otimes (\text{DFT}_m \otimes I_{n/\nu}) \right) \left( (L_m^{\text{mm}} \otimes I_{n/\nu}) \otimes_{\mu} I_{\nu} \right)$$

$$\left( \prod_{i=0}^{\nu-1} T_n^{\text{mm}, i} \right) \left( I_p \otimes (I_{n/\nu} \otimes \text{DFT}_n) \right) \left( I_p \otimes (L_m^{\text{mm}/\nu} \otimes I_{n/\nu}) \right) \left( (L_m^{\text{mm}} \otimes I_{n/\nu}) \otimes_{\mu} I_{\nu} \right)$$

## Vectorization:

$$\frac{(\text{DFT}_{\text{mm}})}{\text{vec}(\nu)} \rightarrow \frac{(\text{DFT}_m \otimes I_n) T_n^{\text{mm}} (I_m \otimes \text{DFT}_n) L_m^{\text{mm}}}{\text{vec}(\nu)}$$

$$\dots$$

$$\rightarrow \frac{(\text{DFT}_m \otimes I_n) T_n^{\text{mm}} (I_m \otimes \text{DFT}_n) L_m^{\text{mm}}}{\text{vec}(\nu) \text{vec}(\nu) \text{vec}(\nu) \text{vec}(\nu)}$$

$$\dots$$

$$\rightarrow (I_{m/\nu} \otimes L_2^{2\nu}) (\text{DFT}_m \otimes I_{n/\nu}) (I_{m/\nu} \otimes L_2^{2\nu})$$

$$\left( I_{m/\nu} \otimes (L_2^{2\nu} \otimes I_n) \right) \left( I_{n/\nu} \otimes (L_2^{2\nu} \otimes I_n) \right) \left( I_2 \otimes L_2^{2\nu} \right) \left( L_2^{2\nu} \otimes I_n \right) (\text{DFT}_n \otimes I_n)$$

$$\left( (L_m^{\text{mm}} \otimes I_2) \otimes I_{\nu} \right) \left( I_{m/\nu} \otimes L_2^{2\nu} \right)$$

## GPUs:

$$\frac{(\text{DFT}_{\nu, \mu})}{\text{gpu}(\nu, \mu)} \rightarrow \frac{\left( \prod_{i=0}^{\nu-1} L_i^{\nu, \mu} (I_{\mu-1} \otimes \text{DFT}_\nu) \right) \left( L_{\mu-1}^{\nu, \mu} (I_{\mu} \otimes T_{\mu-1}^{\nu, \mu}) L_{\mu-1}^{\nu, \mu} \right)}{\text{gpu}(\nu, \mu) \text{vec}(\nu)}$$

$$\dots$$

$$\rightarrow \left( \prod_{i=0}^{\nu-1} (L_i^{\nu, \mu} \otimes I_2) (I_{\mu-1, \nu/2} \otimes \times (\text{DFT}_\nu \otimes I_2) L_2^{2\nu}) T_i \right)$$

$$\left( L_{\mu-1, \nu/2}^{\nu, \mu} \otimes I_2 \right) \left( I_{\mu-1, \nu/2} \otimes \times L_2^{2\nu} \right) \left( R_{\mu-1, \nu/2}^{\nu, \mu} \otimes I_2 \right)$$

## Verilog for FPGAs:

$$\frac{(\text{DFT}_{\nu, \mu})}{\text{stream}(\nu^*)} \rightarrow \frac{\left( \prod_{i=0}^{\nu-1} L_i^{\nu, \mu} (I_{\mu-1} \otimes \text{DFT}_\nu) \right) \left( L_{\mu-1}^{\nu, \mu} (I_{\mu} \otimes T_{\mu-1}^{\nu, \mu}) L_{\mu-1}^{\nu, \mu} \right)}{\text{stream}(\nu^*)}$$

$$\dots$$

$$\rightarrow \frac{\left( \prod_{i=0}^{\nu-1} L_i^{\nu, \mu} (I_{\mu-1} \otimes \text{DFT}_\nu) \right) \left( L_{\mu-1}^{\nu, \mu} (I_{\mu} \otimes T_{\mu-1}^{\nu, \mu}) L_{\mu-1}^{\nu, \mu} \right)}{\text{stream}(\nu^*) \text{stream}(\nu^*)}$$

$$\dots$$

$$\rightarrow \frac{\left( \prod_{i=0}^{\nu-1} L_i^{\nu, \mu} (I_{\mu-1} \otimes (I_{\mu-1} \otimes \text{DFT}_\nu)) \right) \left( L_{\mu-1}^{\nu, \mu} \right)}{\text{stream}(\nu^*) \text{stream}(\nu^*)}$$

- Rigorous, correct by construction
- Overcomes compiler limitations

## Organization

- Spiral: Basic system
- Vectorization
- *General input size*
- Results
- Final remarks

## Challenge: General Size Libraries

### So far:

*Code specialized to fixed input size*

```
DFT_384(x, y) {  
  ...  
  for(i = ...) {  
    t[2i]   = x[2i] + x[2i+1]  
    t[2i+1] = x[2i] - x[2i+1]  
  }  
  ...  
}
```

- Algorithm fixed
- Nonrecursive code

### Challenge:

*Library for general input size*

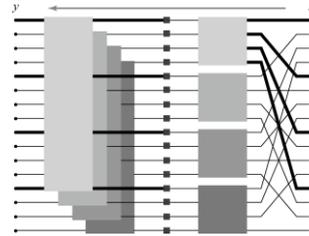
```
DFT(n, x, y) {  
  ...  
  for(i = ...) {  
    DFT_strided(m, x+mi, y+i, 1, k)  
  }  
  ...  
}
```

- Algorithm cannot be fixed
- Recursive code
- Creates many challenges

## Challenge: Recursion Steps

- Cooley-Tukey FFT

$$y = (\text{DFT}_k \otimes I_m) T_m^{km} (I_k \otimes \text{DFT}_m) L_k^{km} x$$



- Implementation that increases locality (e.g., FFTW 2.x)

```
void DFT(int n, cpx *y, cpx *x) {
    int k = choose_dft_radix(n);

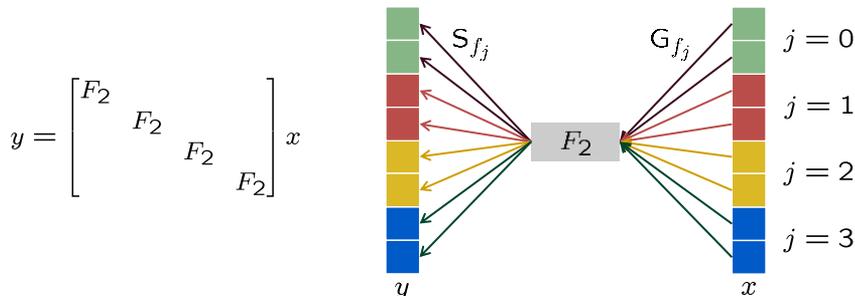
    for (int i=0; i < k; ++i)
        DFTrec(m, y + m*i, x + i, k, 1);
    for (int j=0; j < m; ++j)
        DFTscaled(k, y + j, t[j], m);
}
```

## $\Sigma$ -SPL : Basic Idea

- Four additional matrix constructs:  $\Sigma$ ,  $G$ ,  $S$ ,  $\text{Perm}$ 
  - $\Sigma$  (sum) explicit loop
  - $G_f$  (gather) load data with index mapping  $f$
  - $S_f$  (scatter) store data with index mapping  $f$
  - $\text{Perm}_f$  permute data with the index mapping  $f$

- $\Sigma$ -SPL formulas = matrix factorizations  $\sum_3$

Example:  $y = (I_4 \otimes F_2)x \rightarrow y = \sum_{j=0}^3 S_{f_j} F_2 G_{f_j} x$



# Find Recursion Step Closure

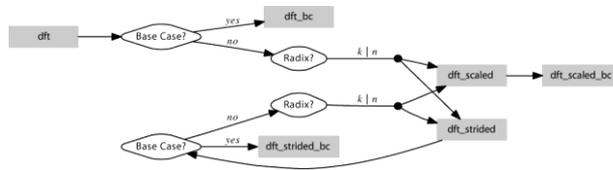
Voronenko, 2008

$$\begin{aligned}
 & \{\text{DFT}_n\} \\
 & \downarrow \\
 & ((\text{DFT}_{n/k} \otimes I_k) T_k^n (I_{n/k} \otimes \text{DFT}_k)) L_{n/k}^n \\
 & \downarrow \\
 & \left( \sum_{i=0}^{k-1} S_{h_{i,k}} \{\text{DFT}_{n/k}\} G_{h_{i,k}} \right) \text{diag}(f) \left( \sum_{j=0}^{n/k-1} S_{h_{j,k,1}} \{\text{DFT}_k\} G_{h_{j,k,1}} \right) \text{perm}(L_{n/k}^n) \\
 & \downarrow \\
 & \sum_{i=0}^{k-1} S_{h_{i,k}} \{\text{DFT}_{n/k}\} \text{diag}(f \circ h_{i,k}) G_{h_{i,k}} \quad \sum_{j=0}^{n/k-1} S_{h_{j,k,1}} \{\text{DFT}_k\} G_{h_{j,n/k}} \\
 & \downarrow \\
 & \sum_{i=0}^{k-1} \{ S_{h_{i,k}} \text{DFT}_{n/k} \text{diag}(f \circ h_{i,k}) G_{h_{i,k}} \} \quad \sum_{j=0}^{n/k-1} \{ S_{h_{j,k,1}} \text{DFT}_k G_{h_{j,n/k}} \}
 \end{aligned}$$

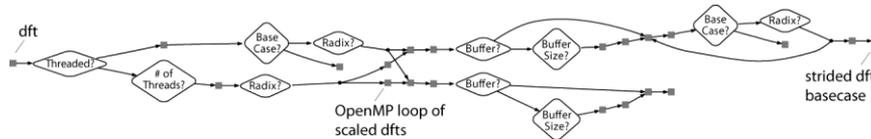
**Repeat until closure**

# Recursion Step Closure: Examples

**DFT: scalar code**



**DFT: full-fledged (vectorized and parallel code)**



## Summary: Complete Automation for Transforms

- **Memory hierarchy optimization**  
Rewriting and search for algorithm selection  
Rewriting for loop optimizations

- **Vectorization**  
Rewriting

- **Parallelization**  
Rewriting

*fixed input size code*

- **Derivation of library structure**

Rewriting  
Other methods

*general input size library*

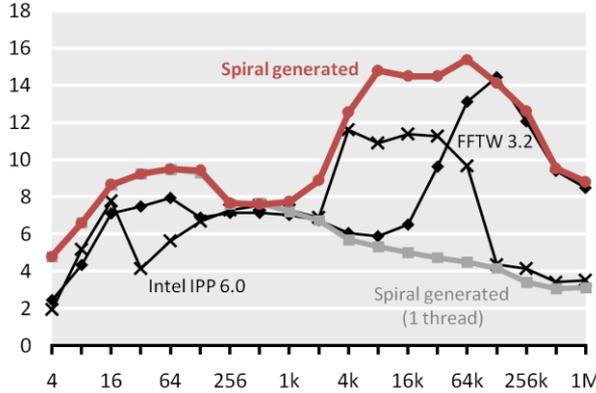
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# DFT on Intel Multicore

Complex DFT (Intel Core i7, 2.66 GHz, 4 cores)

Performance [Gflop/s] vs. input size



$$\begin{aligned} \text{DFT}_n &\rightarrow (\text{DFT}_k \otimes I_m) T_m^n (I_k \otimes \text{DFT}_m) L_k^n \\ \text{DFT}_n &\rightarrow P_{k/2,2m}^\top (\text{DFT}_{2m} \oplus (I_{k/2-1} \otimes C_{2m} \text{rDFT}_{2m}(i/k))) (\text{RDFT}_k \otimes I_m) \\ \text{RDFT}_n &\rightarrow (P_{k/2,2m} \otimes I_2) (\text{RDFT}_{2m} \oplus (I_{k/2-1} \otimes D_{2m} \text{rDFT}_{2m}(i/k))) (\text{RDFT}_k \otimes I_m) \\ \text{rDFT}_{2n}(u) &\rightarrow L_{2n}^{2n} (I_k \otimes \text{rDFT}_{2m}((i+u)/k)) (\text{rDFT}_{2k}(u) \otimes I_m) \end{aligned}$$

5MB vectorized, threaded, general-size, adaptive library  
Spiral

# Generating 100s of FFTWs

PhD thesis Voronenko, 2009

$$\begin{aligned} \text{DFT}_n &\rightarrow P_{k/2,2m}^\top (\text{DFT}_{2m} \oplus (I_{k/2-1} \otimes C_{2m} \text{rDFT}_{2m}(i/k))) (\text{RDFT}'_k \otimes I_m), \quad k \text{ even,} \\ \begin{pmatrix} \text{RDFT}'_n \\ \text{RDFT}'_n \\ \text{DHT}'_n \\ \text{DHT}'_n \end{pmatrix} &\rightarrow (P_{k/2,2m}^\top \otimes I_2) \left( \begin{pmatrix} \text{RDFT}'_{2m} \\ \text{RDFT}'_{2m} \\ \text{DHT}'_{2m} \\ \text{DHT}'_{2m} \end{pmatrix} \oplus (I_{k/2-1} \otimes D_{2m} \begin{pmatrix} \text{rDFT}'_{2m}(i/k) \\ \text{rDFT}'_{2m}(i/k) \\ \text{rDHT}'_{2m}(i/k) \\ \text{rDHT}'_{2m}(i/k) \end{pmatrix}) \right) \begin{pmatrix} \text{RDFT}'_k \\ \text{RDFT}'_k \\ \text{DHT}'_k \\ \text{DHT}'_k \end{pmatrix} \otimes I_m, \quad k \text{ even,} \\ \begin{pmatrix} \text{rDFT}'_{2n}(u) \\ \text{rDHT}'_{2n}(u) \end{pmatrix} &\rightarrow L_{2n}^{2n} \begin{pmatrix} I_k \\ I_k \end{pmatrix} \begin{pmatrix} \text{rDFT}'_{2m}((i+u)/k) \\ \text{rDHT}'_{2m}((i+u)/k) \end{pmatrix} \begin{pmatrix} \text{rDFT}'_{2k}(u) \\ \text{rDHT}'_{2k}(u) \end{pmatrix} \otimes I_m, \\ \text{RDFT-3}_n &\rightarrow (Q_{k/2,2m}^\top \otimes I_2) (I_k \otimes \text{rDFT}_{2m}(i+1/2/k)) (\text{RDFT-3}_k \otimes I_m), \quad k \text{ even,} \\ \text{DCT-2}_n &\rightarrow P_{k/2,2m}^\top (\text{DCT-2}_{2m} K_{2m}^{2m} \oplus (I_{k/2-1} \otimes N_{2m} \text{RDFT-3}_{2m}^\top)) B_n (L_{k/2}^{n/2} \otimes I_2) (I_m \otimes \text{RDFT}'_k) Q_{m/2,k}, \\ \text{DCT-3}_n &\rightarrow \text{DCT-2}_n^\top, \\ \text{DCT-4}_n &\rightarrow Q_{k/2,2m}^\top (I_{k/2} \otimes N_{2m} \text{RDFT-3}_{2m}^\top) B'_n (L_{k/2}^{n/2} \otimes I_2) (I_m \otimes \text{RDFT-3}_k) Q_{m/2,k}. \\ \text{DFT}_n &\rightarrow (\text{DFT}_k \otimes I_m) T_m^n (I_k \otimes \text{DFT}_m) L_k^n, \quad n = km \\ \text{DFT}_n &\rightarrow P_n (\text{DFT}_k \otimes \text{DFT}_m) Q_n, \quad n = km, \text{gcd}(k,m) = 1 \\ \text{DFT}_p &\rightarrow R_p^\top (I_1 \oplus \text{DFT}_{p-1}) D_p (I_1 \oplus \text{DFT}_{p-1}) R_p, \quad p \text{ prime} \\ \text{DCT-3}_n &\rightarrow (I_m \oplus J_m) L_m^n (\text{DCT-3}_m(1/4) \oplus \text{DCT-3}_m(3/4)) \\ &\quad \cdot (F_2 \otimes I_m) \begin{bmatrix} I_m & 0 \\ 0 & -J_{m-1} \end{bmatrix}, \quad n = 2m \\ \text{DCT-4}_n &\rightarrow S_n \text{DCT-2}_n \text{diag}_{0 \leq k < n} (1/(2 \cos((2k+1)\pi/4n))) \\ \text{IMDCT}_{2m} &\rightarrow (J_m \oplus I_m \oplus I_m \oplus J_m) \left( \begin{bmatrix} 1 & \\ & -1 \end{bmatrix} \otimes I_m \right) \oplus \begin{bmatrix} -1 & \\ & -1 \end{bmatrix} \otimes I_m \otimes J_{2m} \text{DCT-4}_{2m} \\ \text{WHT}_{2k} &\rightarrow \prod_{i=1}^l (I_{2^{k_1+\dots+k_{i-1}}} \otimes \text{WHT}_{2^{k_i}} \otimes I_{2^{k_1+\dots+k_i}}), \quad k = k_1 + \dots + k_l \\ \text{DFT}_2 &\rightarrow F_2 \\ \text{DCT-2}_2 &\rightarrow \text{diag}(1, 1/\sqrt{2}) F_2 \\ \text{DCT-4}_2 &\rightarrow J_2 R_{13\pi/8} \end{aligned}$$

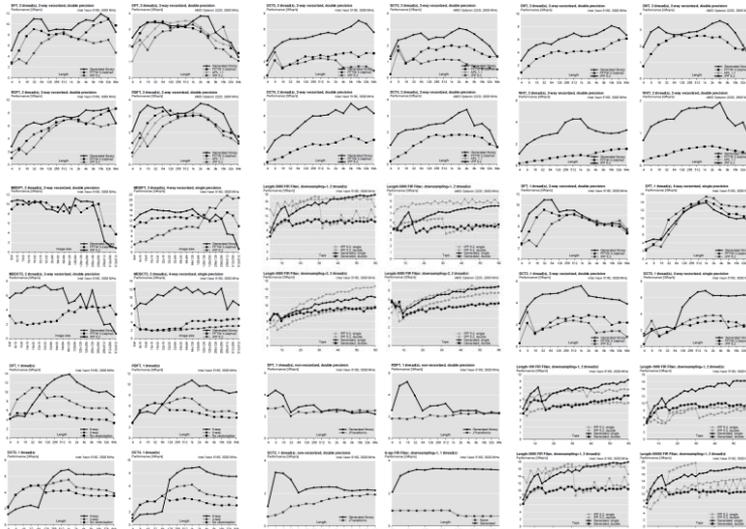
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Transform	Code size	
	non-parallelized	parallelized
<i>no vectorization</i>		
DFT	13.1 KLOC / 0.59 MB	10.3 KLOC / 0.45 MB
RDFT	8.5 KLOC / 0.36 MB	8.8 KLOC / 0.39 MB
DHT	9.1 KLOC / 0.40 MB	9.4 KLOC / 0.39 MB
DCT-2	12.0 KLOC / 0.55 MB	12.4 KLOC / 0.57 MB
DCT-3	12.0 KLOC / 0.56 MB	12.3 KLOC / 0.59 MB
DCT-4	6.8 KLOC / 0.33 MB	7.1 KLOC / 0.35 MB
WHT	5.6 KLOC / 0.21 MB	—
<i>2-way vectorization</i>		
DFT	14.8 KLOC / 0.73 MB	15.0 KLOC / 0.74 MB
RDFT	15.6 KLOC / 0.76 MB	16.0 KLOC / 0.81 MB
scaled RDFT	16.0 KLOC / 0.78 MB	—
DHT	16.9 KLOC / 0.83 MB	17.2 KLOC / 0.87 MB
DCT-2	20.7 KLOC / 1.10 MB	21.0 KLOC / 1.09 MB
DCT-3	27.9 KLOC / 1.56 MB	28.2 KLOC / 1.59 MB
DCT-4	7.8 KLOC / 0.47 MB	8.1 KLOC / 0.50 MB
WHT	6.9 KLOC / 0.32 MB	5.8 KLOC / 0.26 MB
FIR Filter	167 KLOC / 7.75 MB	120 KLOC / 5.12 MB
Downsampled FIR Filter	100 KLOC / 4.2 MB	68 KLOC / 2.76 MB
<i>4-way vectorization</i>		
DFT	17.9 KLOC / 1.09 MB	18.2 KLOC / 1.11 MB
RDFT	16.2 KLOC / 0.86 MB	16.5 KLOC / 0.91 MB
scaled RDFT	16.5 KLOC / 0.88 MB	—
DHT	17.9 KLOC / 1.02 MB	18.3 KLOC / 1.04 MB
DCT-2	23.3 KLOC / 1.50 MB	23.6 KLOC / 1.53 MB
DCT-3	32.0 KLOC / 2.17 MB	32.3 KLOC / 2.20 MB
DCT-4	8.3 KLOC / 0.63 MB	8.6 KLOC / 0.66 MB
WHT	8.5 KLOC / 0.53 MB	6.9 KLOC / 0.4 MB
2D DFT	20.6 KLOC / 1.32 MB	20.8 KLOC / 1.33 MB
2D DCT-2	27.0 KLOC / 2.1 MB	27.2 KLOC / 2.11 MB
FIR Filter	109 KLOC / 5.69 MB	74 KLOC / 3.44 MB
Downsampled FIR Filter	151 KLOC / 7.7 MB	92 KLOC / 4.61 MB

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# Computer generated Functions for Intel IPP 6.0



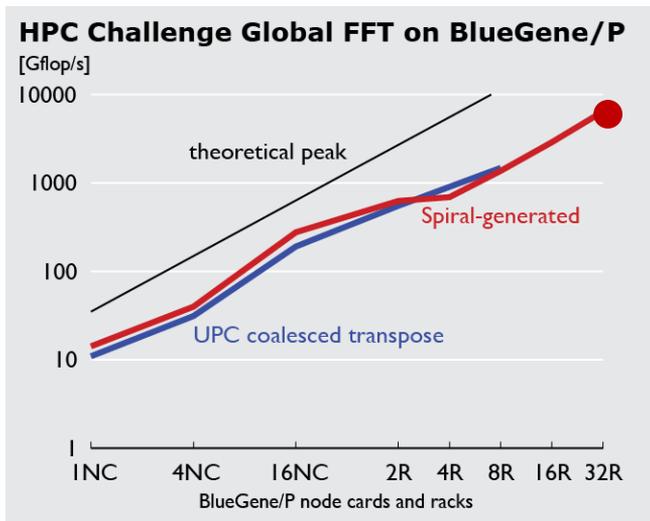
**3984 C functions**  
**1M lines of code**

*Transforms: DFT (fwd+inv), RDFT (fwd+inv), DCT2, DCT3, DCT4, DHT, WHT*  
*Sizes: 2-64 (DFT, RDFT, DHT); 2-powers (DCTs, WHT)*  
*Precision: single, double*  
*Data type: scalar, SSE, AVX (DFT, DCT), LRB (DFT)*

**Computer generated**

*Results: SpiralGen Inc.*

# Very Large Scale: BG/P



**6.4 Tflop/s**  
**32 racks**  
**= 32K node cards**  
**= 128K cores**

2010 HPC Challenge Class I Award, Almasi et al.

# Organization

- Spiral: Basic system
- Vectorization
- General input size
- Results
- *Final remarks*

# Spiral: Summary

- **Spiral:**  
Successful approach to automating the development of computing software

Commercial proof-of-concept



- **Key ideas:**  
*Algorithm knowledge:*  
 Domain specific symbolic representation  
  
*Platform knowledge:*  
 Tagged rewrite rules, SIMD specification

DFT<sub>64</sub>



```
void df64(float *Y, float *X) {
    __m512 U912, U913, U914, U915, ...
    __m512 *a2153, *a2155;
    a2153 = ((__m512 *) X); a1107 = *(a2153);
    a1108 = *((a2153 + 4)); t1323 = __mm512_add_ps(a1107, a1108);
    t1204 = __mm512_sub_ps(a1107, a1108);
    #many more lines*
    U926 = __m512_swispcovr_r32(,);
    a1121 = __mm512_madd231_ps(__mm512_mask_or_pi(
        __mm512_swt_1to16_ps(0.70710678118654757).0xAAAA, a2154, U926), t1341);
    __mm512_mask_sub_ps(__mm512_swt_1to16_ps(0.70710678118654757), ...,
        __mm512_swispcovr_r32(t1341, __MM_SWI2_R32_CBAB));
    U927 = __m512_swispcovr_r32
    #many more lines*
}
```

$$DFT_4 \rightarrow (DFT_2 \otimes I_2) T_2^4 (I_2 \otimes DFT_2) L_2^4$$

$$\frac{I_m \otimes A_n}{\text{sm}(p, \mu)} \rightarrow I_p \otimes \left( \frac{I_m}{p} \otimes A_n \right)$$