

Evaluating Homomorphic Operations on a Real-World Processing-In-Memory System

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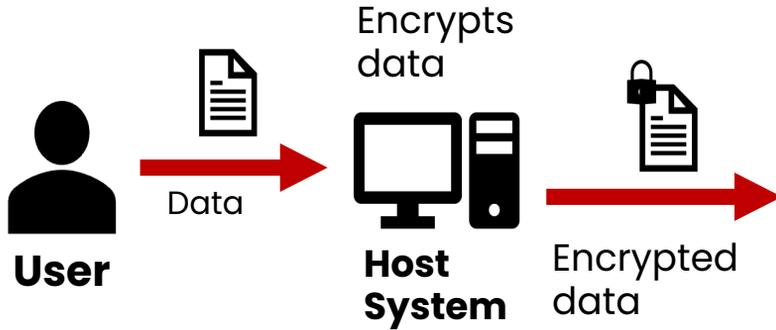
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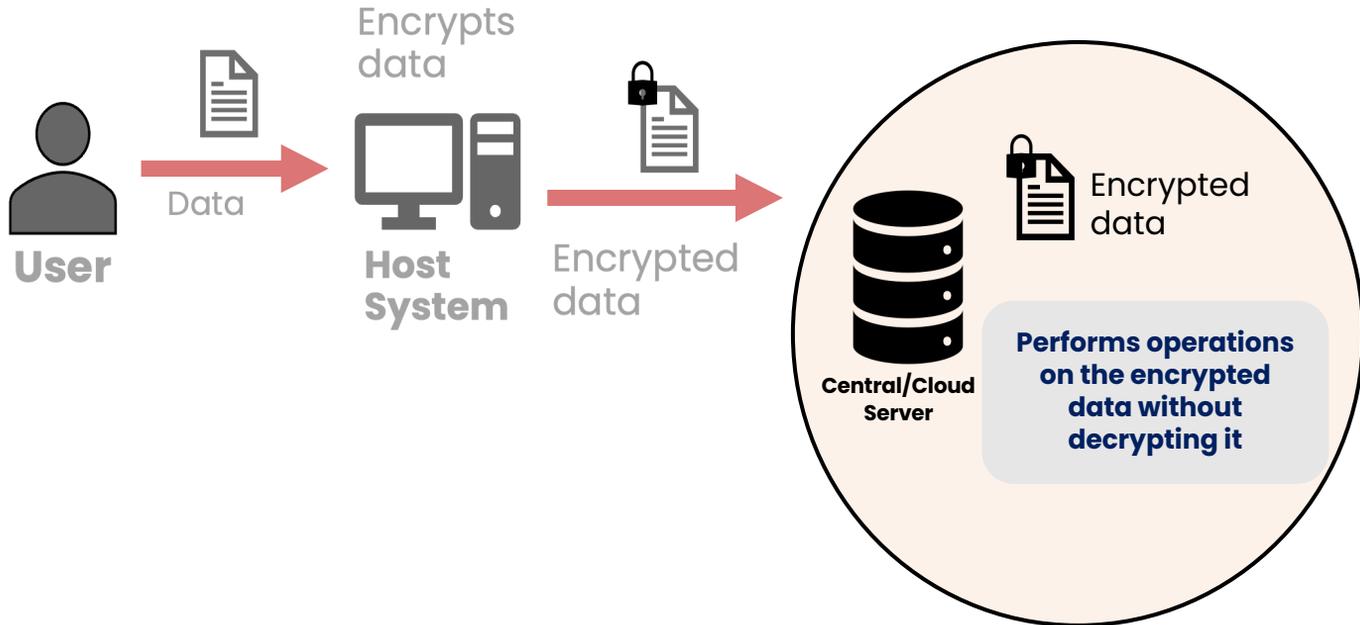
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Homomorphic Encryption

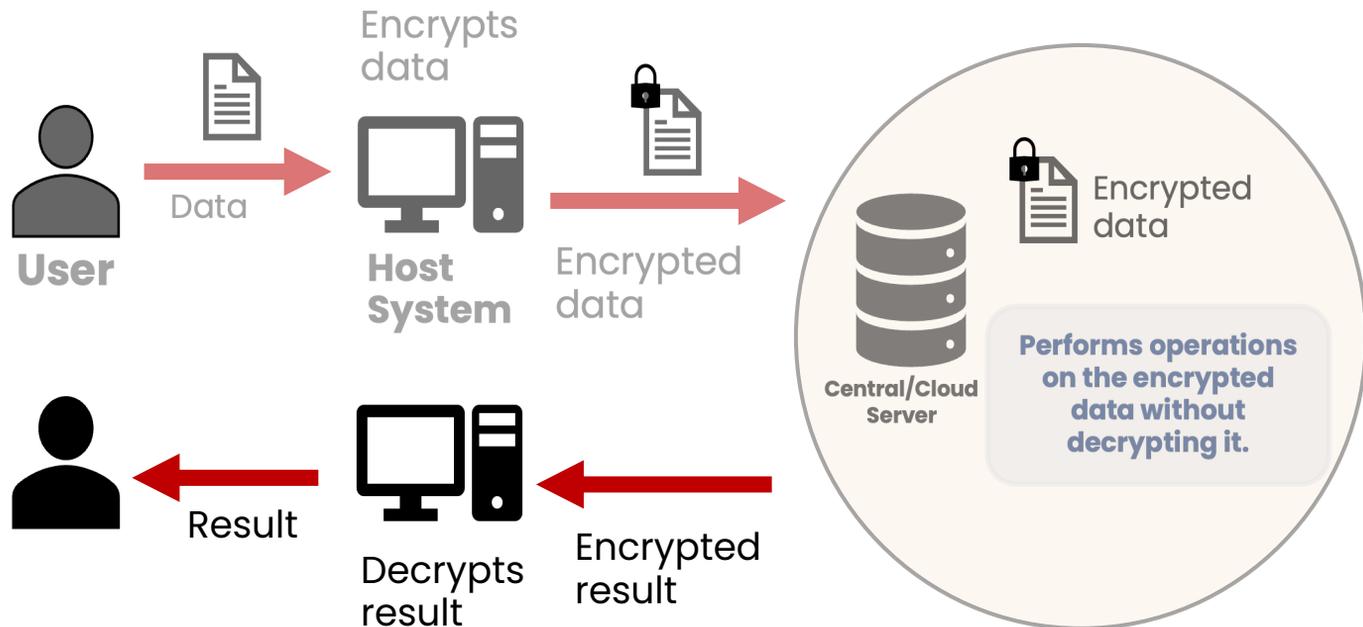
Homomorphic Encryption



Homomorphic Encryption



Homomorphic Encryption



Motivation

Homomorphic operations suffer from **large memory capacity and data movement bottlenecks**



Acceleration Techniques



Motivation

These approaches face challenges in resource limitations, data movement, and practical implementation

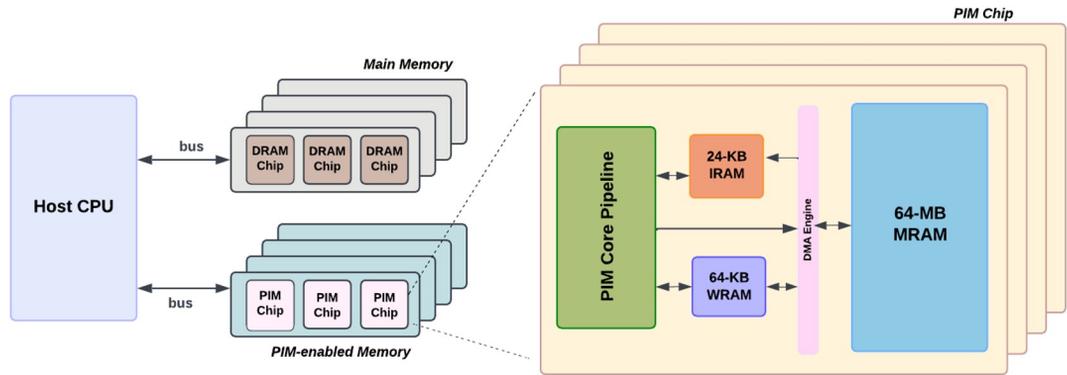


Our Goal

Evaluate the suitability of real-world general-purpose processing-in-memory (PIM) architectures to perform homomorphic operations.

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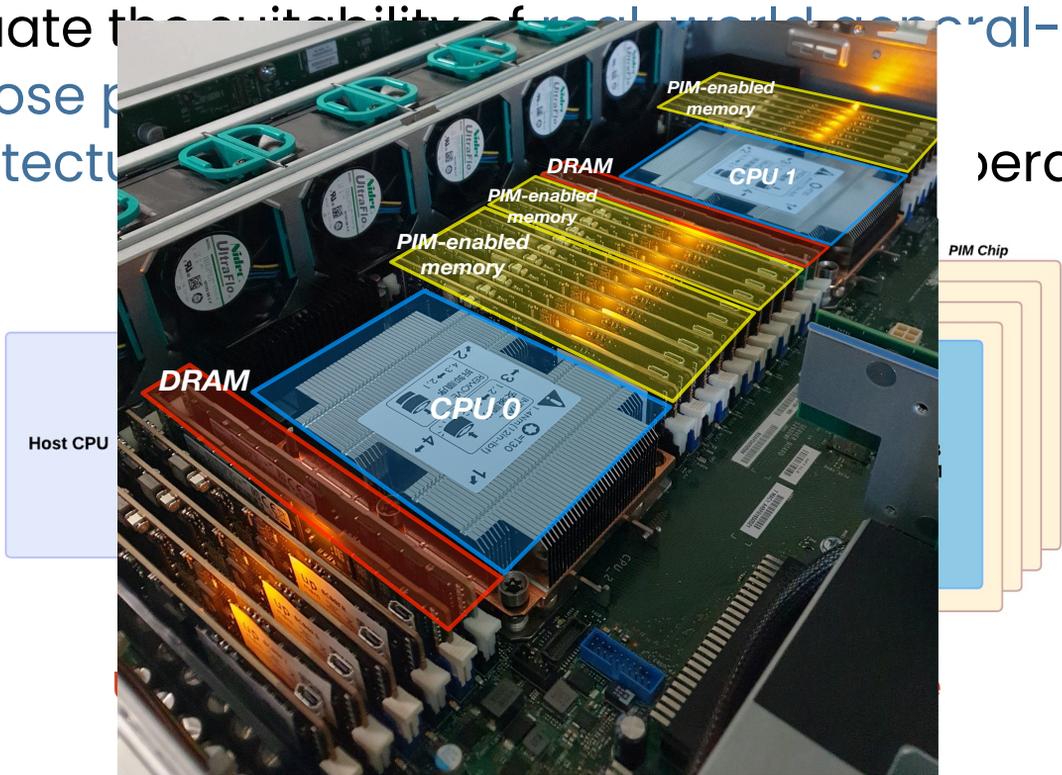
Evaluate the suitability of real-world general-purpose processing-in-memory (PIM) architectures to perform homomorphic operations.



UPMEM: First Real World PIM Architecture

Our Goal

Evaluate the suitability of general-purpose architectures for neural operations.



Evaluation Methodology

①

Evaluation of **homomorphic addition and multiplication** on UPMEM PIM system

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Evaluation of **homomorphic addition and multiplication** on UPMEM PIM system



②

Evaluation with statistical workloads
(**mean, variance, linear regression**)

Evaluation Methodology

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Evaluation of **homomorphic addition and multiplication** on UPMEM PIM system

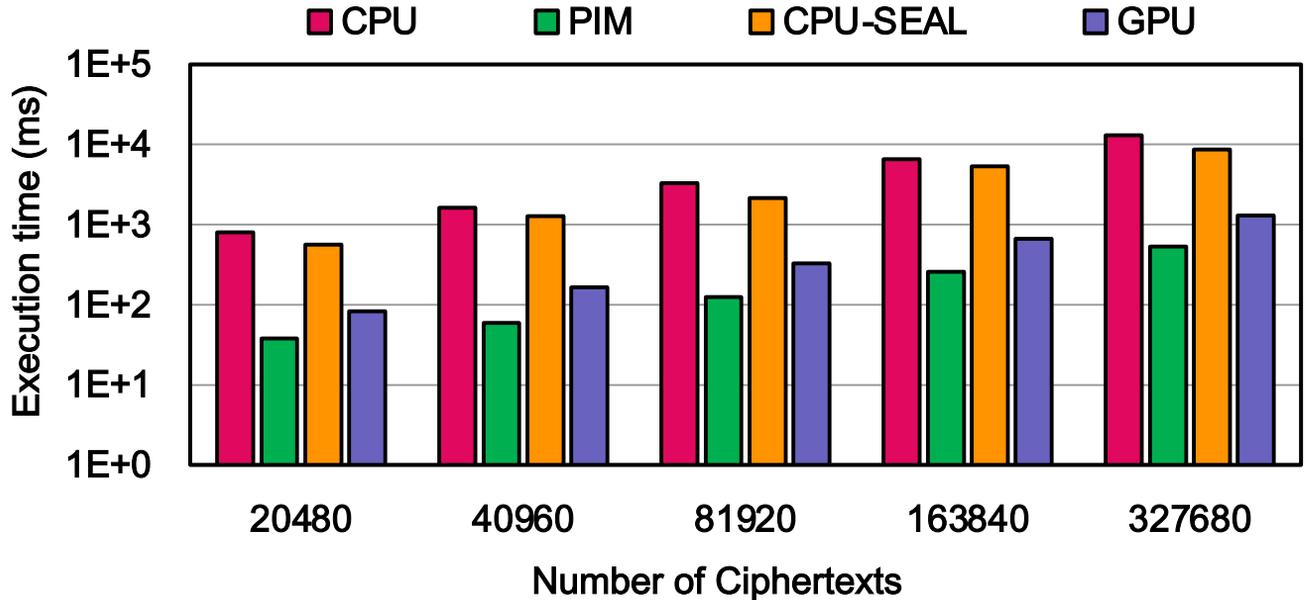
②

Evaluation with statistical workloads
(**mean, variance, linear regression**)

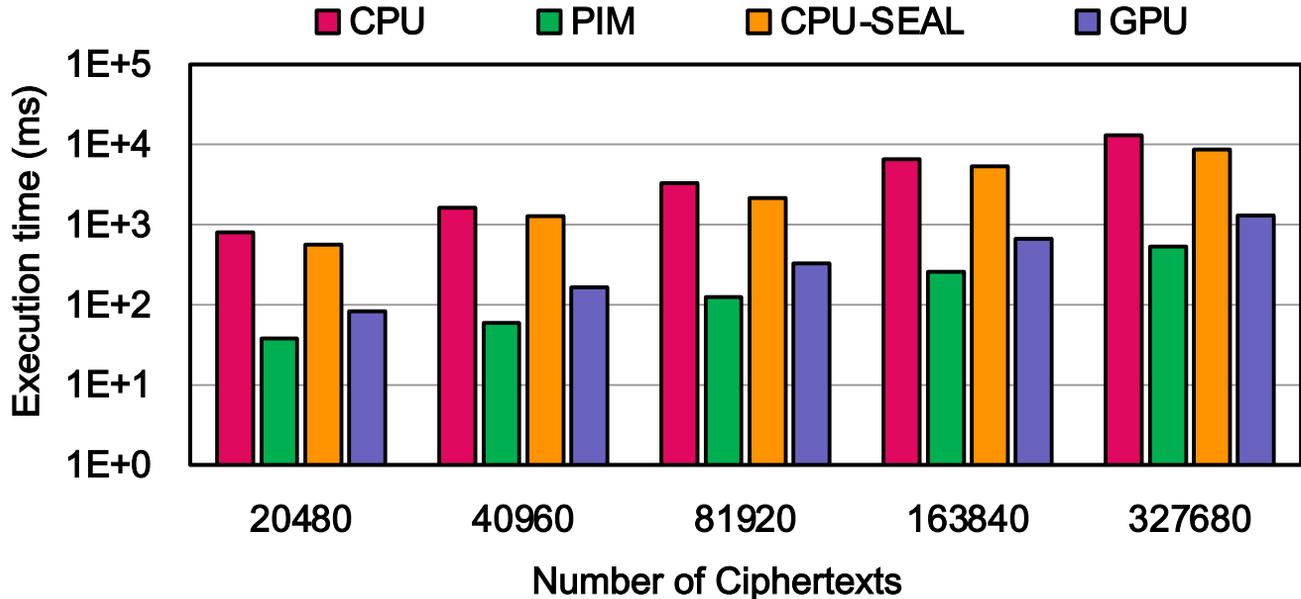
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Comparison with custom **CPU and GPU** libraries
and an optimized **CPU library (SEAL)**

Evaluation: Homomorphic Addition

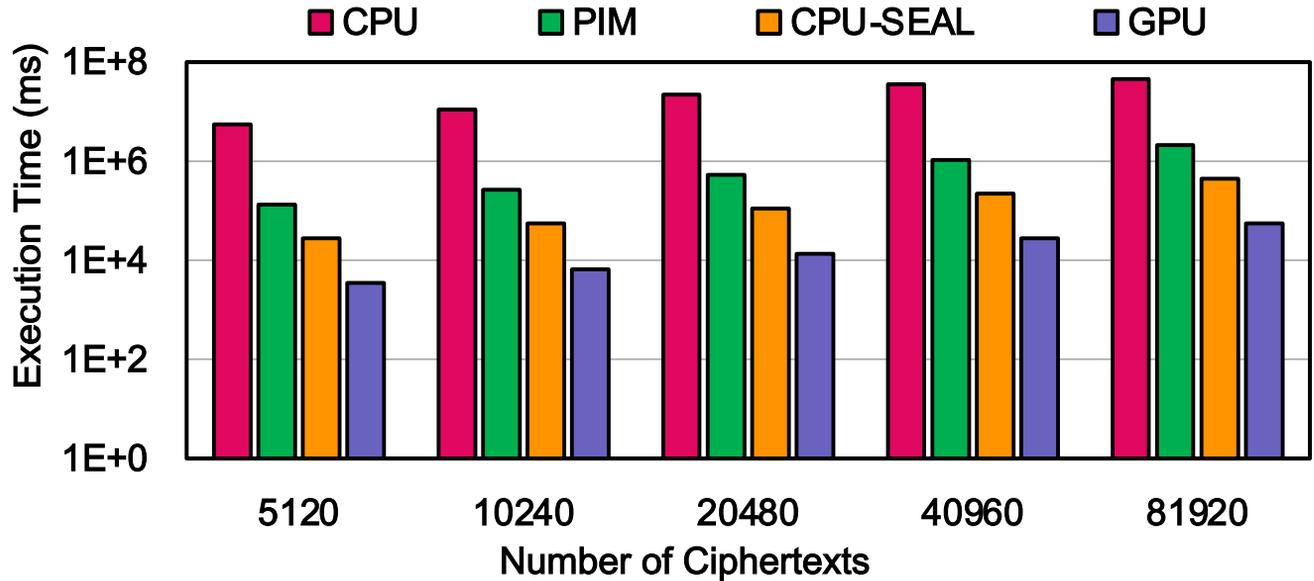


Evaluation: Homomorphic Addition

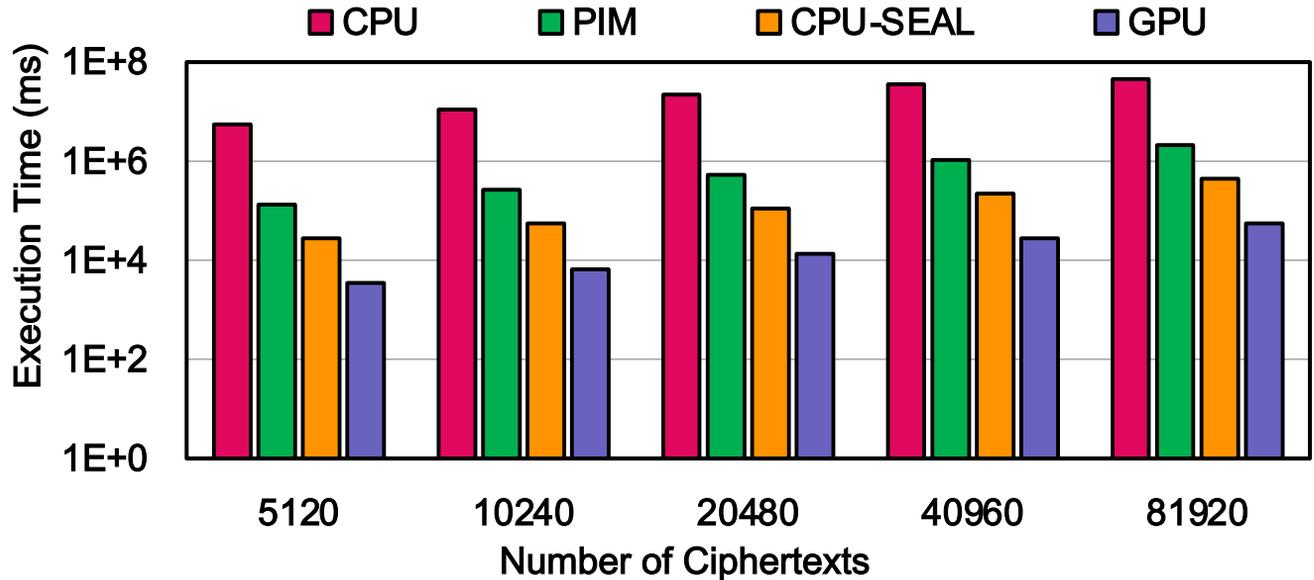


50 - 100× speedup provided by PIM over CPU
2 - 15× speedup over GPU

Evaluation: Homomorphic Multiplication

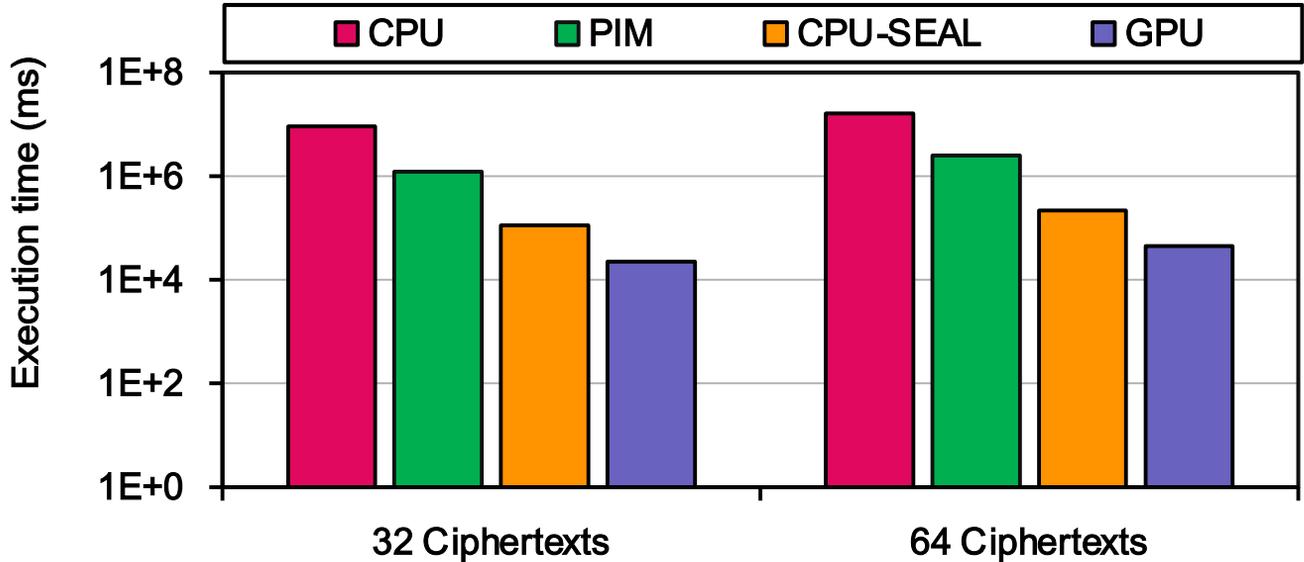


Evaluation: Homomorphic Multiplication

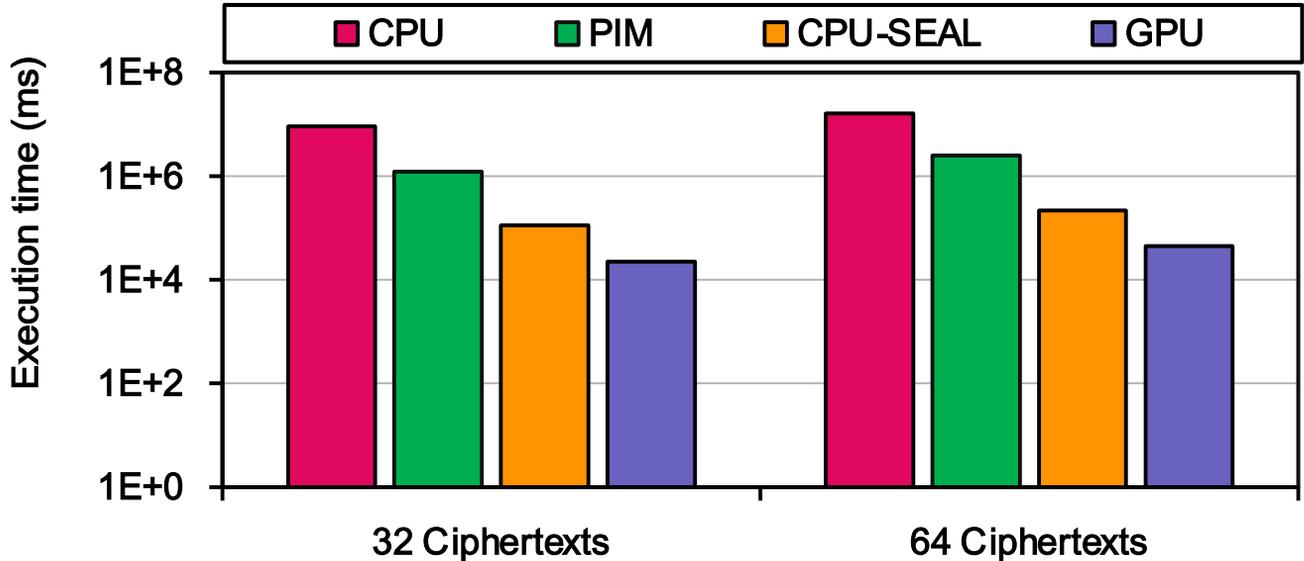


**PIM lags 10 - 15× behind the GPU
due to the lack of native multiplication support**

Evaluation: Linear Regression



Evaluation: Linear Regression



PIM is 6.4–7.5x faster than the custom CPU implementation

CPU-SEAL and GPU are faster than PIM

Key Takeaways

①

UPMEM PIM system natively supports 32-bit integer addition and outperforms CPU and GPU for homomorphic *addition*

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The **lack of native support for 32-bit integer multiplication hampers the performance** of PIM for homomorphic *multiplication*.

Key Takeaways

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UPMEM PIM system natively supports **32-bit integer addition** and outperforms CPU and GPU for homomorphic ***addition***

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The **lack of native support for 32-bit integer multiplication** hampers the performance of PIM for homomorphic ***multiplication***.

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The **computational power of PIM scales with memory capacity** via the addition of more memory banks and PIM cores